

Vishay Siliconix

High Speed, Low Voltage, 3 Ω , Quad SPDT CMOS Analog Switch

DESCRIPTION

The DG2706 is a high speed, low voltage, low On-resistance, quad SPDT (single pole double throw) analog switch. It operates from a 1.65 V to 4.3 V single power supply and achieves 3 Ω switch On-resistance. When turned on, each switch conducts equally in both directions. Its switch on resistance flatness is 0.6 Ω and channel to channel matching is of 0.3 Ω when powered with single 3.15 V supply. All channels guaranteed break before make switching.

Control logic input has 0.5 V to 1.65 V logic threshold. It features a 190 MHz - 3 dB bandwidth, - 90 dB crosstalk and - 70 dB off-isolation at 1 MHz.

The DG2706 is an ideal fit for low voltage battery powered devices switching audio, video, multi-media data streams, and control signals between different functional circuits or ports.

The DG2707 comes in a small miniQFN-16 lead package (1.8 mm x 2.6 mm x 0.75 mm). As a committed partner to community and the environment, Vishay Siliconix manufactures this product with the lead(Pb)-free device terminations and is 100 % RoHS compliant.

FEATURES

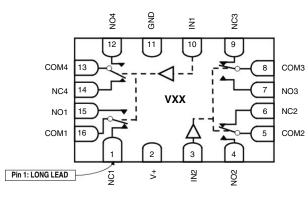
- Operation voltage range: 1.65 V to 4.3 V
- Guaranteed On-resistance: 3.0 Ω at 3.15 V
- Low voltage logic threshold
- Low crosstalk: 70 dB
- High off-isolation: 90 dB
- Ultra small package: miniQFN16 of 1.8 mm x 2.6 mm

APPLICATIONS

- Dual SIM card switch
- A/V and analog signal routing
- Battery operated devices
- Data acquisition systems
- · Communications systems
- · Medical and ATE equipments

FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION

miniQFN-16L



Top View

Device Marking: V<u>XX</u> Traceability Code: V is DG2706DN <u>XX</u> = Date/Lot

ORDERING INFORMATION					
Temp. Range	Package	Part Number			
- 40 °C to 85 °C	miniQFN-16	DG2706DN-T1-E4			



COMPLIANT



TRUTH TABLE DG2706 QUAD SPDT, miniQFN-16L					
Select Input		On Switches			
IN1 (Pin 10)	IN2 (Pin 3)	Description (Pin)	Common (Pin)		
0	Х	NC1 (Pin 1)	COM1 (Pip 16)		
1	Х	NO1 (Pin 15)	COM1 (Pin 16)		
0	Х	NC4 (Pin 14)	COM4 (Din 12)		
1	Х	NO4 (Pin 12)	COM4 (Pin 13)		
Y	0	NC2 (Pin 6)			
Х	1	NO2 (Pin 4)	COM2 (Pin 5)		
х	0	NC3 (Pin 9)			
	1	NO3 (Pin 7)	COM3 (Pin 3)		

Parameter		Limit	Unit	
	V+	- 0.3 to 5.0	v	
Reference to GND	IN, COM, NC, NO ^a	- 0.3 to (V+ + 0.3)	V	
Current (Any terminal except NO, NC or COM)		30	mA	
Continuous Current (NO, NC, or COM)		± 250		
Peak Current (Pulsed at 1 ms, 10 % Duty Cycle)		± 500		
Storage Temperature (D Suffix)		- 65 to 150	°C	
Thermal Resistance (Package) ^b	miniQFN-16	152	°C/W	
Power Dissipation (Package) ^b	miniQFN-16 ^{c, d}	525	mW	

Notes:

a. Signals on NC, NO, or COM or IN exceeding V+ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.

b. All leads welded or soldered to PC board.

c. Derate 6.6 mW/°C above 70 $^\circ\text{C}$

d. Manual soldering with iron is not recommended for leadless components. The miniQFN-16 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper lip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.



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		Test Conditions		Limits - 40 °C to 85 °C				
Parameter	Symbol	Otherwise Unless Specified	Temp. ^b	Min. ^d	Typ. ^c	Max. ^d Unit		
Analog Switch								
Analog Signal Range ^e	V _{ANALOG}	R _{DS(on)}	Full	0		V+	V	
On-Resistance	Б	V+ = 3.15 V, I _{NO/NC} = 10 mA, V _{COM} = 1.0 V	Room		3	5.5	Ω	
On-mesistance	R _{DS(on)}	V = 3.13 V, $NO/NC = 10$ MA, $VCOM = 1.0$ V	Full			6		
R _{ON} Match	$\Delta R_{(ON)}$	V+ = 3.15 V, $I_{NO/NC}$ = 10 mA, V_{COM} = 1.0 V	Room		0.3			
R _{ON} Resistance Flatness	R _{ON}	V + = 3.15 V, $I_{NO/NC}$ = 10 mA,	Room		0.6			
	luce success		Room	- 5		5	nA	
Channel Off Leakage	INO/NC(off)	$V + = 3.6 V$, $V_{NO/NC} = 0.5 V/3 V$,	Full	- 10		10		
Current	.	V _{COM} = 3 V/0.5 V	Room	- 5		5		
	I _{COM(off)}		Full	- 10		10		
Channel-On Leakage	1	V+ = 3.6 V, V _{NO/NC} , V _{COM} = 3 V/0.5 V	Room	- 10		10	-	
Current	I _{COM(on)}	$V + = 3.0 V, V_{NO/NC}, V_{COM} = 3 V/0.3 V$	Full	- 20		20		
Digital Control								
Input High Voltage	V _{INH}		Full	1.65			v	
Input Low Voltage	V _{INL}		Full			0.4		
Input Current	I _{INL} or I _{INH}	$V_{IN} = 0 \text{ or } V+$	Full	- 1		1	μA	
Dynamic Characteristics								
Break-Before-Make Time	toout		Room		1		ns	
Dieak-Deloie-Make Time	t _{BBM} t _{ON(EN)} t _{OFF(EN)}		Full	5				
Enable Turn-On Time		V_{NO} , V_{NC} = 1.5 V, R_1 = 50 Ω , C_1 = 35 pF	Room		20	45		
		$v_{\rm NO}, v_{\rm NC} = 1.5 v, 11 = 30 s_2, 01 = 30 pr$	Full			55		
Enable Turn-Off Time			Room		15	35		
			Full			45		
Charge Injection ^d	Q _{INJ}	C_L = 1 nF, R_{GEN} = 0 Ω , $V_{NC/NO}$ = 2 V	Room		3		рС	
Off-Isolation ^d	OIRR	V+ = 3.15 V, f = 1 MHz, R _L = 50 Ω, C _L = 5 pF	Room		- 70		dB	
Crosstalk ^{d, f}	X _{TALK}	$v_{+} = 5.15 v_{+} = 1 \text{ winz}, \text{ m}_{-} = 50 \text{ sz}, \text{ O}_{-} = 5 \text{ pr}_{-}$	Room		- 90		UD	
Bandwidth ^d	BW	V+ = 3.15 V, R _L = 50 Ω , C _L = 5 pF, - 3 dB	Room		190		MHz	
Total Harmonic Distortion ^d	THD	V+ = 3.15 V, R_{LOAD} = 600 Ω	Room		0.02		%	
N _O , N _C Off Capacitance ^d	CS _{NC(off)}				16			
	CS _{NO(on)}	V+ = 3.15 V, f = 1 MHz	Room		15		pF	
Channel-On Capacitance ^d					31			
Power Supply								
Power Supply Range	V+			1.65		4.3	V	
Power Supply Current	l+	V _{IN} = 0 or V+	Full			1	μA	

Notes:

a. Room = 25 °C, Full = as determined by the operating suffix.

b. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.

c. Typical values are for design aid only, not guaranteed nor subject to production testing.

d. Guarantee by design, not subjected to production test.

e. V_{IN} = input voltage to perform proper function.

f. Crosstalk measured between channels.

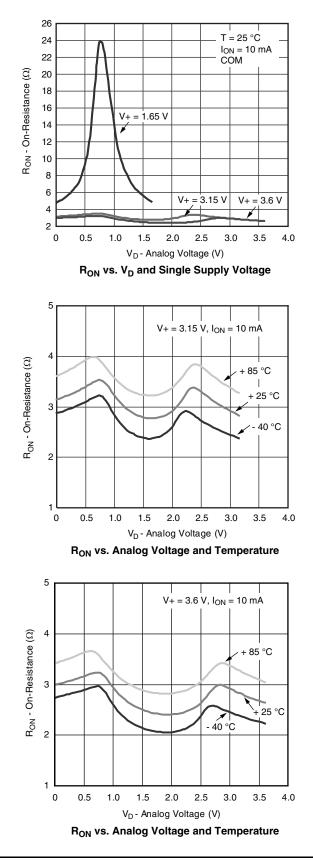
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

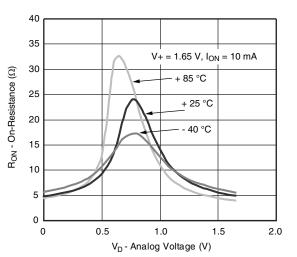
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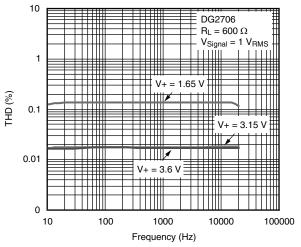


TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)

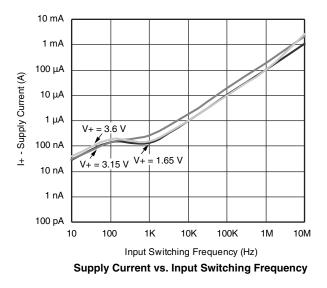




R_{ON} vs. Analog Voltage and Temperature

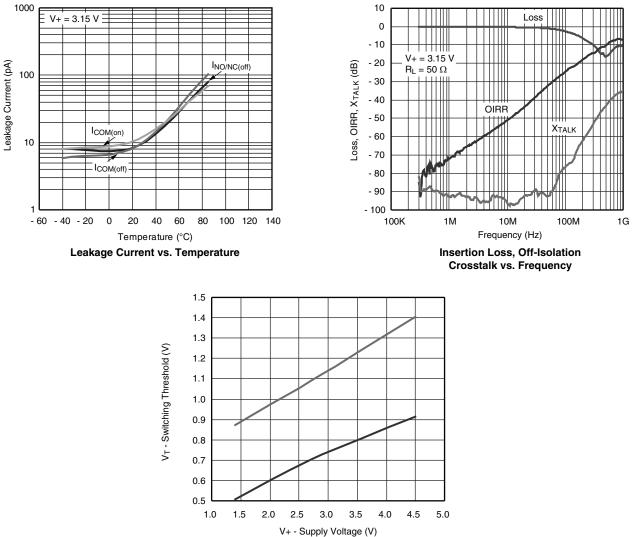


Switching Threshold vs. Supply Voltage





TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)

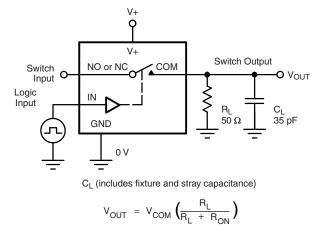


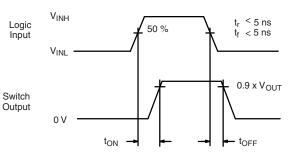
Switching Threshold vs. Supply Voltage

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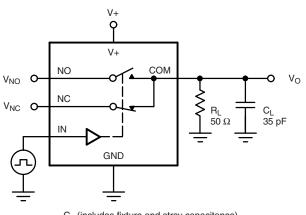


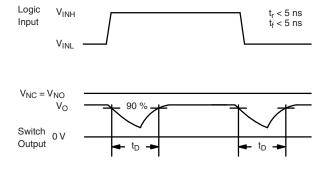


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Logic "1" = Switch on Logic input waveforms inverted for switches that have the opposite logic sense.

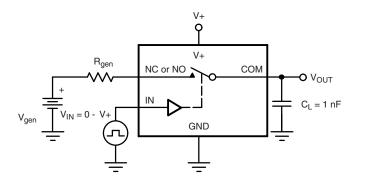


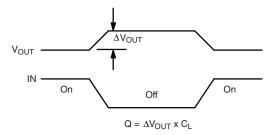




C_L (includes fixture and stray capacitance)

Figure 2. Break-Before-Make Interval





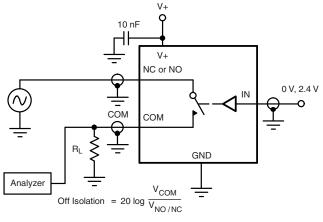
IN depends on switch configuration: input polarity determined by sense of switch.





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TEST CIRCUITS





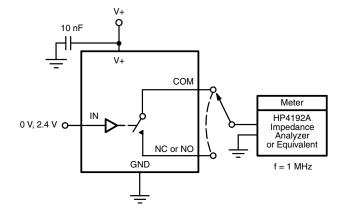


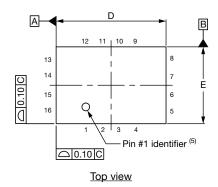
Figure 5. Channel Off/On Capacitance

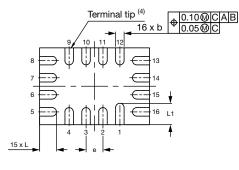
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?68392.



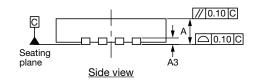
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Thin miniQFN16 Case Outline





Bottom view



DIMENSIONS -	MILLIMETERS ⁽¹⁾			INCHES		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
А	0.50	0.55	0.60	0.020	0.022	0.024
A1	0	-	0.05	0	-	0.002
A3	0.15 ref. 0.006 ref.			0.006 ref.		
b	0.15	0.20	0.25	0.006	0.008	0.010
D	2.50	2.60	2.70	0.098	0.102	0.106
е	0.40 BSC			0.016 BSC		
E	1.70	1.80	1.90	0.067	0.071	0.075
L	0.35	0.40	0.45	0.014	0.016	0.018
L1	0.45	0.50	0.55	0.018	0.020	0.022
N ⁽³⁾	16				16	
Nd ⁽³⁾	4			4		
Ne ⁽³⁾	4 4					

Notes

⁽¹⁾ Use millimeters as the primary measurement.

- ⁽²⁾ Dimensioning and tolerances conform to ASME Y14.5M. 1994.
- ⁽³⁾ N is the number of terminals. Nd and Ne is the number of terminals in each D and E site respectively.

 $^{(4)}$ Dimensions b applies to plated terminal and is measured between 0.15 mm and 0.30 mm from terminal tip.

⁽⁵⁾ The pin 1 identifier must be existed on the top surface of the package by using identification mark or other feature of package body.

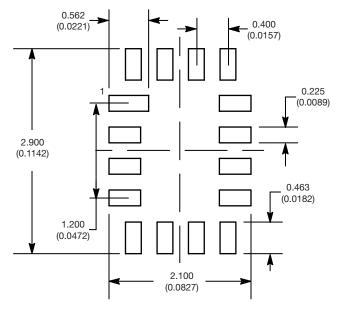
⁽⁶⁾ Package warpage max. 0.05 mm.

ECN: T16-0226-Rev. B, 09-May-16 DWG: 6023

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RECOMMENDED MINIMUM PADS FOR MINI QFN 16L



Mounting Footprint Dimensions in mm (inch)



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