samtec

QSTRIP®

(0.635 mm) .025"

MIT SERIES

MIXED TECHNOLOGY HEADER

SPECIFICATIONS

For complete specifications and recommended PCB layouts see www.samtec.com?MIT

Insulator Material:
Liquid Crystal Polymer
Contact Material:
Phosphor Bronze
Plating:
Au or Sn over
50 µ" (1.27 µm) Ni
Operating Temp Range:
-55 °C to +125 °C
Voltage Rating:
275 VAC
Max Cycles:
100
RoHS Compliant:
Yes

PROCESSING

Lead–Free Solderable:
Yes
SMT Lead Coplanarity:
(0.10 mm) .004" max (019-057)
Board Stacking:
For applications requiring more than two connectors per board contact ipg@samtec.com

RECOGNITIONS

For complete scope of recognitions see www.samtec.com/quality

FILE NO. E111594

ALSO AVAILABLE
(MOQ Required)

- 11 mm, 16 mm, 18.75 mm and 22 mm stack height
- 30 µ" (0.76 µm) Gold
- Differential Pair and "Partitionable" (combine differential & single-ended banks in same connector) available.
- 76, 95, 114 and 133 positions per row

Board Mates:
MIS

Standoffs:
SO

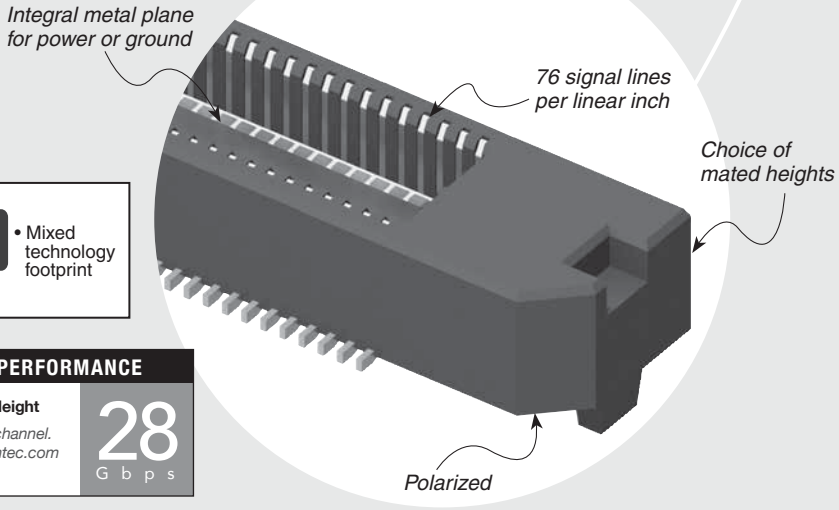
RUGGEDIZED BY SAMTEC • Mixed technology footprint

HIGH-SPEED CHANNEL PERFORMANCE

MIT/MIS @ 5 mm Mated Stack Height

Rating based on Samtec reference channel. For full SI performance data visit Samtec.com or contact SIG@samtec.com

28 Gbps



MIT	–	NO. OF POSITIONS PER ROW	–	LEAD STYLE	–	PLATING OPTION	–	D	–	OTHER OPTION

–019, –038, –057
(38 total positions per bank)

Specify LEAD STYLE from chart

–F
= Gold Flash on Signal Pins and Ground Plane, Matte Tin on tails

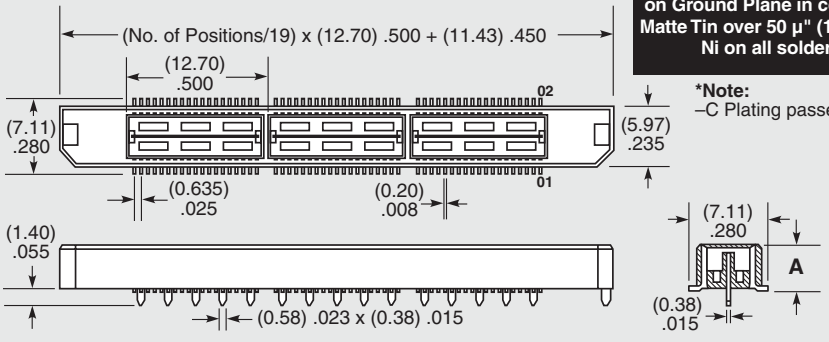
–L
= 10 µ" (0.25 µm) Gold on Signal Pins and Ground Plane, Matte Tin on tails

–C*
= Electro-Polished Selective 50 µ" (1.27 µm) min Au over 150 µ" (3.81 µm) Ni on Signal Pins in contact area, 10 µ" (0.25 µm) min Au over 50 µ" (1.27 µm) Ni on Ground Plane in contact area, Matte Tin over 50 µ" (1.27 µm) min Ni on all solder tails

–K
= (7.00 mm) .275" DIA Polyimide film Pick & Place Pad

–TR
= Tape & Reel

LEAD STYLE	A
–01	(4.27) .168
–02	(7.26) .286



***Note:**
–C Plating passes 10 year MFG testing

MATED HEIGHT*		
MIS LEAD STYLE	MIT LEAD STYLE	
	–01	–02
–01	(5.00) .197	(8.00) .315

*Processing conditions will affect mated height. See SO Series for board space tolerances.

Note: Rugged through-hole ground plane soldered to board (requires paste-over-hole, not press-fit) for added retention to PCB.

Note:
Some lengths, styles and options are non-standard, non-returnable.

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Samtec:

MIT-019-06-F-D MIT-019-05-F-D-K MIT-038-06-F-D MIT-114-03-F-D MIT-019-01-L-D-K MIT-038-01-L-D-K MIT-076-06-H-D-K MIT-076-06-L-D MIT-133-03-H-D MIT-076-01-F-D-K-TR MIT-076-01-L-D-TR MIT-133-03-F-D MIT-019-02-L-D-K MIT-019-03-F-D-K MIT-038-05-F-D MIT-057-02-L-D-K-TR MIT-057-05-F-D-K MIT-076-01-L-D-K MIT-095-06-F-D MIT-019-04-H-D MIT-019-05-L-D-K MIT-038-03-F-D-K MIT-057-03-F-D-K MIT-076-02-L-D-K-TR MIT-076-04-F-D-K MIT-114-04-F-D MIT-114-05-F-D MIT-038-01-H-D MIT-038-02-F-D MIT-038-02-F-D-K MIT-048-01-F-D-DP MIT-076-01-F-D-K MIT-133-06-F-D MIT-019-01-F-D-K-TR MIT-019-02-L-D-TR MIT-019-06-L-D-K MIT-038-02-H-D MIT-057-06-L-D-K MIT-038-01-F-D-TR MIT-038-05-L-D-K MIT-057-02-F-D MIT-076-02-F-D MIT-076-04-H-D-K-TR MIT-076-05-F-D-K MIT-019-01-F-D-K MIT-038-02-F-D-TR MIT-038-02-L-D-K MIT-057-06-F-D-K MIT-095-04-H-D MIT-114-05-L-D MIT-019-01-H-D-K MIT-019-02-F-D-K MIT-019-02-F-D-K-TR MIT-076-01-F-D MIT-076-05-L-D-K MIT-076-06-F-D-K MIT-133-04-F-D MIT-019-03-F-D-K-TR MIT-038-04-F-D-K MIT-038-04-H-D-K-TR MIT-057-03-L-D-K MIT-057-04-F-D MIT-057-05-F-D MIT-076-03-L-D-K-TR MIT-114-01-L-D-K MIT-114-03-F-D-K MIT-019-04-F-D-K MIT-019-05-F-D MIT-038-04-F-D MIT-076-03-F-D MIT-038-01-F-D-K MIT-038-02-L-D-TR MIT-038-05-F-D-K MIT-057-01-F-D-K MIT-057-02-F-D-K MIT-057-05-L-D-K MIT-076-04-H-D MIT-076-05-F-D MIT-114-01-F-D-K MIT-019-02-H-D MIT-038-04-L-D-TR MIT-038-06-F-D-K MIT-076-02-H-D MIT-095-05-L-D MIT-133-02-L-D-K MIT-114-05-F-D-K MIT-133-01-F-D MIT-038-01-L-D MIT-057-02-F-D-K-TR MIT-076-02-L-D MIT-095-01-L-D MIT-114-02-F-D MIT-038-03-F-D MIT-057-02-L-D MIT-076-03-L-D MIT-095-02-L-D MIT-057-01-F-D MIT-114-03-L-D MIT-038-05-L-D MIT-038-06-L-D