

## High Drive Fundamental Quartz Crystal Oscillator

### ■GENERAL DESCRIPTION

The NJU6360 series is a C-MOS fundamental quartz crystal oscillator that consists of an oscillation amplifier, 3-stage divider and 3-state output buffer.

The 5-stage divider generates only one frequency selected of  $f_0, f_0/2, f_0/4, f_0/8, f_0/16$  and  $f_0/32$  by internal circuits is output.

The oscillation amplifier is realized very low stand-by current using NAND circuit.

The 3-state output buffer is C-MOS compatible and can drive 50pF(@5V) C-MOS load.

### ■PACKAGE OUTLINE

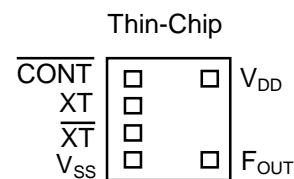


NJU6360XC-D

### ■FEATURES

- Operating Voltage 2.7 to 5.5V
- Maximum Oscillation Frequency 50MHz
- Low Operating Current
- High Fan-out  $I_{OH}/I_{OL}=8mA@3.3V$   
 $I_{OH}/I_{OL}=16mA@5.0V$
- 5-Stage Divider Maximum Divider  $f_0/32$
- Oscillation Stop and Output Stand-by Function
- 3-State Output Buffer
- Oscillation Capacitors  $C_g$  and  $C_d$  on-chip
- Package Outline Thin-Chip
- C-MOS Technology

### ■PAD LOCATION



### ■LINE-UP TABLE

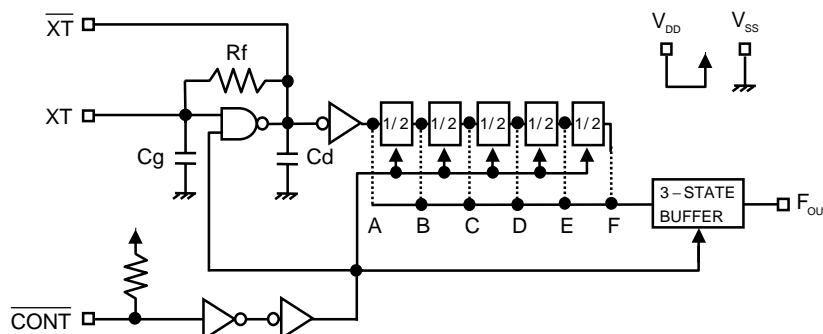
Type No.	F <sub>OUT</sub>	Internal Connect	C <sub>g</sub> /C <sub>d</sub>
NJU6360	A	$f_0$	Connected A Line 15/15pF
	B	$f_0/2$	Connected B Line 15/15pF
	C	$f_0/4$	Connected C Line 15/15pF
	D	$f_0/8$	Connected D Line 15/15pF
	E	$f_0/16$	Connected E Line 15/15pF
	F	$f_0/32$	Connected F Line 15/15pF

### ■COORDINATES

No	Pad Name	X	Y
1	$\overline{CONT}$	-178	231
2	XT	-178	77
3	$\overline{XT}$	-178	-77
4	V <sub>SS</sub>	-178	-231
5	F <sub>OUT</sub>	206	-231
6	V <sub>DD</sub>	206	231

Starting Point: Chip Center Unit[um]  
 Chip Size: 0.7x0.75mm  
 Thin-Chip Thickness(-D): 200±20um  
 Pad Size: 90x90um

### ■BLOCK DIAGRAM



## ■ TERMINAL DESCRIPTION

SYMBOL	FUNCTION
	Oscillation and 3-state Output Buffer Control
$\overline{\text{CONT}}$	$\overline{\text{CONT}}$ $F_{\text{OUT}}$
	H or OPEN      Output either one frequency selected of $f_0$ , $f_0/2$ , $f_0/4$ , $f_0/8$ , $f_0/16$ and $f_0/32$ Note1)
	L      Oscillation Stop and High impedance Output
$\overline{\text{XT}}$	Quartz Crystal Connecting Terminals
$\overline{\text{XT}}$	
$V_{\text{SS}}$	$V_{\text{SS}}=0\text{V}$
$F_{\text{OUT}}$	Frequency Output
$V_{\text{DD}}$	$V_{\text{DD}}=2.5/3.0\text{V}/5.0\text{V}$

Note1) Refer to the line-up table.

## ■ ABSOLUTE MAXIMUM RATINGS

( $T_a=25^\circ\text{C}$ )

PARAMETER	SYMBOL	RATING	UNIT
Supply Voltage	$V_{\text{DD}}$	-0.5 to +7.0	V
Input Voltage	$V_{\text{IN}}$	$V_{\text{SS}}-0.5$ to $V_{\text{DD}}+0.5$	V
Output Voltage	$V_{\text{O}}$	-0.5 to $V_{\text{DD}}+0.5$	V
Input Current	$I_{\text{IN}}$	$\pm 10$	MA
Output Current	$I_{\text{O}}$	$\pm 25$	MA
Operating Temperature Range	$T_{\text{opr}}$	-40 to +85	$^\circ\text{C}$
Storage Temperature Range	$T_{\text{stg}}$	-55 to +125	$^\circ\text{C}$

Note2) If the supply voltage( $V_{\text{DD}}$ ) is less than 7.0V, the input voltage must not over the  $V_{\text{DD}}$  level though 7.0V is limit specified.

Note3) Decoupling capacitor should be connected between  $V_{\text{DD}}$  and  $V_{\text{SS}}$  due to the stabilized operation for the circuit.

## ■ ELECTRICAL CHARACTERISTICS

(Ta=25°C)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Operating Voltage	V <sub>DD</sub>		2.7		5.5	V

(V<sub>DD</sub>=3.3V, Ta=25°C)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Operating Current	I <sub>DD</sub>	A version, fosc=16MHz, C <sub>L</sub> =30pF			8	mA
		B version, fosc=16MHz, C <sub>L</sub> =30pF			6	
		C version, fosc=16MHz, C <sub>L</sub> =30pF			4	
		D version, fosc=16MHz, C <sub>L</sub> =30pF			3	
		E version, fosc=16MHz, C <sub>L</sub> =30pF			3	
		F version, fosc=16MHz, C <sub>L</sub> =30pF			3	
Oscillation Stopping Current	I <sub>STB</sub>	$\overline{\text{CONT}} = V_{SS}$ , No load		2	5	uA
Stand-by Current	I <sub>st</sub>	$\overline{\text{CONT}} = \text{XT} = V_{SS}$ , No load Note4)			1	uA
Input Voltage	V <sub>IH</sub>		2.31		3.3	V
	V <sub>IL</sub>		0		0.99	V
Output Current	I <sub>OH</sub>	V <sub>OH</sub> =2.97V	8			mA
	I <sub>OL</sub>	V <sub>OL</sub> =0.33V	8			mA
Input Current	I <sub>IN</sub>	$\overline{\text{CONT}} = 0.8V_{DD}$		10.0	15.0	uA
		$\overline{\text{CONT}} = 0.2V_{DD}$		1.8	3.0	uA
3-state Off Leakage Current	I <sub>OZ</sub>	$\overline{\text{CONT}} = V_{SS}$ , F <sub>OUT</sub> = V <sub>DD</sub> or V <sub>SS</sub>			±0.1	uA
Feedback Resistance	R <sub>f</sub>			255		KΩ
Internal Capacitor	C <sub>g</sub> /C <sub>d</sub>	fosc=16MHz, A/B/C/D/E/F version		15/15		pF
Maximum Oscillation Frequency	F <sub>MAX</sub>		50			MHz
Output Signal Symmetry	SYM	C <sub>L</sub> =15pF, @V <sub>DD</sub> /2	45	50	55	%
		C <sub>L</sub> =30pF, @V <sub>DD</sub> /2	45	50	55	
Output Signal Rise Time	tr	C <sub>L</sub> =15pF, 10% to 90%		3	6	ns
		C <sub>L</sub> =30pF, 10% to 90%		4	8	
Output Signal Fall Time	tf	C <sub>L</sub> =15pF, 90% to 10%		3	6	ns
		C <sub>L</sub> =30pF, 90% to 10%		4	8	
Output Disable time	T <sub>PLZ</sub>	C <sub>L</sub> =15pF, R <sub>UP</sub> =10kΩ			150	Ns
Output Enable Time	T <sub>PZL</sub>	C <sub>L</sub> =15pF, R <sub>UP</sub> =10kΩ			150	Ns

Note4) Excluding input current on  $\overline{\text{CONT}}$  Terminal.

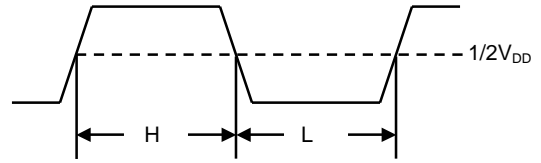
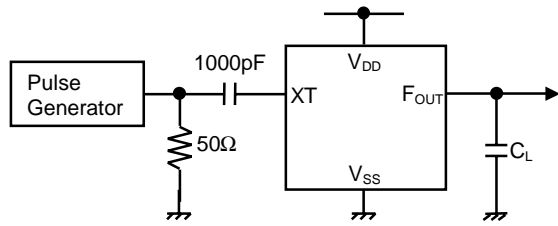
(V<sub>DD</sub>=5.0V, T<sub>a</sub>=25°C)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Operating Current	I <sub>DD</sub>	A version, fosc=16MHz, C <sub>L</sub> =50pF			15	mA
		B version, fosc=16MHz, C <sub>L</sub> =50pF			11	
		C version, fosc=16MHz, C <sub>L</sub> =50pF			9	
		D version, fosc=16MHz, C <sub>L</sub> =50pF			7	
		E version, fosc=16MHz, C <sub>L</sub> =50pF			7	
		F version, fosc=16MHz, C <sub>L</sub> =50pF			7	
Oscillation Stopping Current	I <sub>STB</sub>	CONT = V <sub>SS</sub> , No load		5	10	UA
Stand-by Current	I <sub>st</sub>	CONT = XT = V <sub>SS</sub> , No load Note4)			1	UA
Input Voltage	V <sub>IH</sub>		3.5		5.0	V
	V <sub>IL</sub>	V <sub>OH</sub> =4.5V	0		1.5	V
Output Current	I <sub>OH</sub>	V <sub>OL</sub> =0.5V	12			mA
	I <sub>OL</sub>	CONT = 0.8V <sub>DD</sub>	12			mA
Input Current	I <sub>IN</sub>	CONT = 0.2V <sub>DD</sub>		27.0	40.0	UA
		CONT = V <sub>SS</sub> , F <sub>OUT</sub> = V <sub>DD</sub> or V <sub>SS</sub>		5.5	8.0	uA
3-state Off Leakage Current	I <sub>OZ</sub>				±0.1	uA
Feedback Resistance	R <sub>f</sub>			255		KΩ
Internal Capacitor	C <sub>g</sub> /C <sub>d</sub>	fosc=16MHz, A/B/C/D/E/F version		15/15		pF
Maximum Oscillation Frequency	F <sub>MAX</sub>		50			MHz
Output Signal Symmetry	SYM	C <sub>L</sub> =15pF, @V <sub>DD</sub> /2	45	50	55	%
		C <sub>L</sub> =50pF, @V <sub>DD</sub> /2	45	50	55	
Output Signal Rise Time	tr	C <sub>L</sub> =15pF, 10% to 90%		3	6	ns
		C <sub>L</sub> =50pF, 10% to 90%		4	8	
Output Signal Fall Time	tf	C <sub>L</sub> =15pF, 90% to 10%		3	6	ns
		C <sub>L</sub> =50pF, 90% to 10%		4	8	
Output Disable time	T <sub>PLZ</sub>	C <sub>L</sub> =15pF, R <sub>UP</sub> =10kΩ			100	ns
Output Enable Time	T <sub>PZL</sub>	C <sub>L</sub> =15pF, R <sub>UP</sub> =10kΩ			100	ns

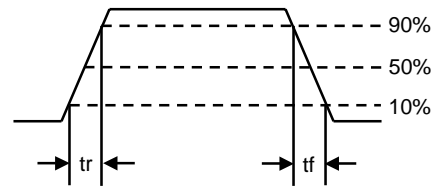
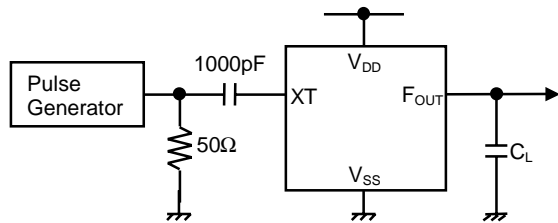
Note4) Excluding input current on CONT Terminal.

MEASUREMENT CIRCUITS

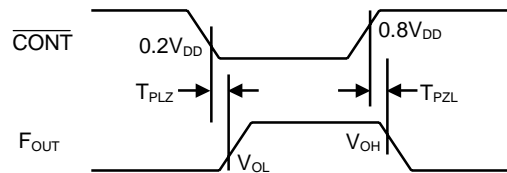
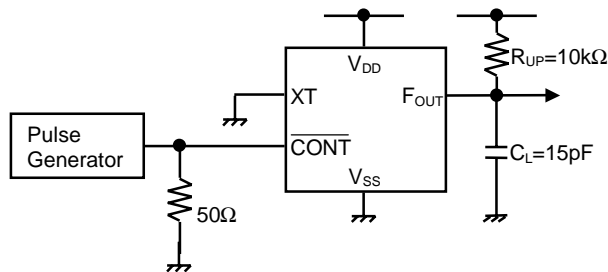
(1) Output Signal Symmetry ( $C_L=15/30/50\text{pF}$ )



(2) Output Signal Rise/Fall Time ( $C_L=15/30/50\text{pF}$ )



(3) Output Disable/Enable Time ( $C_L=15\text{pF}, R_{UP}=10\text{k}\Omega$ )



[CAUTION]  
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