

### WP1384AD/SRD

3.4mm Single-Level Circuit Board Indicator



### DESCRIPTIONS

- The Super Bright Red source color devices are made with Gallium Aluminum Arsenide Red Light Emitting Diode
- · Electrostatic discharge and power surge could damage the LEDs
- · It is recommended to use a wrist band or anti-electrostatic glove when handling the LEDs
- · All devices, equipments and machineries must be electrically grounded

#### **FEATURES**

- · Ideal for card edge status indication
- · High reliability-life measured in years
- Housing UL rating: 94V-0
- Housing material: Type 66 nylon
- RoHS compliant

#### **APPLICATIONS**

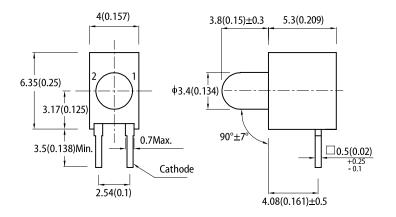
- Status indicator
- Illuminator
- Signage applications
- · Decorative and entertainment lighting
- · Commercial and residential architectural lighting

#### **ATTENTION**

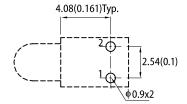
Observe precautions for handling electrostatic discharge sensitive devices



### PACKAGE DIMENSIONS



#### **Recommended PCB Layout**



Notes: 1. All dimensions are in millimeters (inches)

Tolerance is ±0.25(0.01") unless otherwise noted.
 Lead spacing is measured where the leads emerge from the package.

4. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice

#### **SELECTION GUIDE**

Part Number	Emitting Color	Lens Type	lv (mcd) @ 20mA <sup>[2]</sup>		Viewing Angle <sup>[1]</sup>	
Fait Number	(Material)	Lens Type	Min.	Тур.	201/2	
WP1384AD/SRD	Super Bright Red (GaAIAs)	Red Diffused	150	300	201	
			*40	*90	60°	

Notes

- 1. 01/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.
  2. Luminous intensity / luminous flux: +/-15%.
  \* Luminous intensity value is traceable to CIE127-2007 standards.

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#### ELECTRICAL / OPTICAL CHARACTERISTICS at T<sub>A</sub>=25°C

Parameter	Symbol	Emitting Color	Value		Unit
Farameter		Emitting Color	Тур.	Max.	Unit
Wavelength at Peak Emission $I_F$ = 20mA	$\lambda_{peak}$	Super Bright Red	655	-	nm
Dominant Wavelength $I_F$ = 20mA	$\lambda_{dom}$ <sup>[1]</sup>	Super Bright Red	640	-	nm
Spectral Bandwidth at 50% $\Phi$ REL MAX I <sub>F</sub> = 20mA	Δλ	Super Bright Red	20	-	nm
Capacitance	С	Super Bright Red	45	-	pF
Forward Voltage $I_F$ = 20mA	V <sub>F</sub> <sup>[2]</sup>	Super Bright Red	1.85	2.5	V
Reverse Current (V <sub>R</sub> = 5V)	I <sub>R</sub>	Super Bright Red	-	10	uA

Notes:

The dominant wavelength (λd) above is the setup value of the sorting machine. (Tolerance λd : ±1nm.)
 Forward voltage: ±0.1V.
 Wavelength value is traceable to CIE127-2007 standards.
 Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

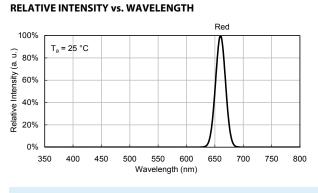
#### ABSOLUTE MAXIMUM RATINGS at T<sub>A</sub>=25°C

Parameter	Symbol	Value	Unit	
Power Dissipation	PD	75	mW	
Reverse Voltage	V <sub>R</sub>	5	V	
Junction Temperature	Tj	115	°C	
Operating Temperature	T <sub>op</sub>	-40 to +85	°C	
Storage Temperature	T <sub>stg</sub>	-40 to +85	°C	
DC Forward Current	l <sub>F</sub>	30	mA	
Peak Forward Current	I <sub>FM</sub> <sup>[1]</sup>	155	mA	
Electrostatic Discharge Threshold (HBM)	-	3000	V	
Lead Solder Temperature <sup>[2]</sup>		260°C For 3 Seconds		
Lead Solder Temperature <sup>[3]</sup>		260°C For 5 Seconds		

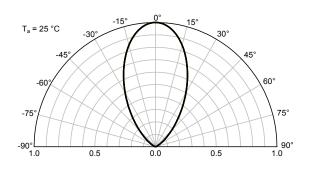
Notes: 1. 1/10 Duty Cycle, 0.1ms Pulse Width. 2. 2mm below package base. 3. 5mm below package base. 4. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

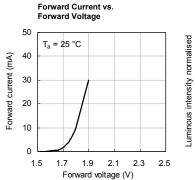
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#### **TECHNICAL DATA**

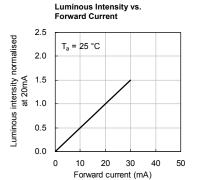


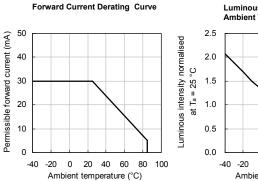
#### SPATIAL DISTRIBUTION



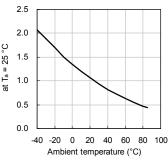


#### **SUPER BRIGHT RED**

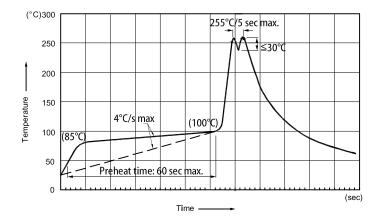




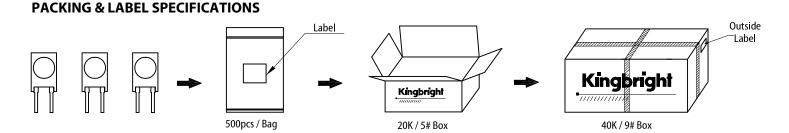




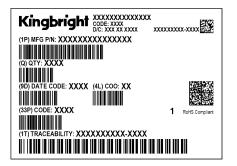
#### **RECOMMENDED WAVE SOLDERING PROFILE**



- Notes: 1. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260  $^\circ\mathrm{C}$
- 2. Peak wave soldering temperature between 245°C ~ 255°C for 3 sec (5 sec max).
- Do not apply stress to the epoxy resin while the temperature is above 85°C.
  Fixtures should not incur stress on the component when mounting and during soldering process.
- SAC 305 solder alloy is recommended.
  No more than one wave soldering pass.



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#### PRECAUTIONS

#### **Storage Conditions**

- 1. Avoid continued exposure to the condensing moisture environment and keep the product away from rapid transitions in ambient temperature.
- 2. LEDs should be stored with temperature  $\leq$  30°C and relative humidity < 60%.
- 3. Product in the original sealed package is recommended to be assembled within 72 hours of opening. Product in opened package for more than a week should be baked for 30 (+10/-0) hours at 85 ~ 100°C.

#### **LED Mounting Method**

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement.

Lead-forming may be required to insure

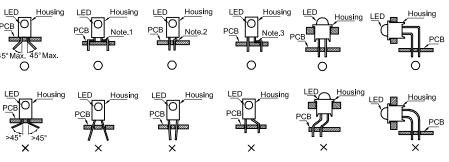
the lead pitch matches the hole pitch.

Refer to the figure below for proper lead forming procedures.

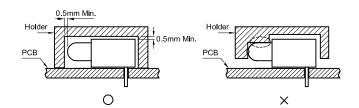
Note 1-3: Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits.

#### Lead Forming Procedures

- 1. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering
- 2. The tip of the soldering iron should never touch the lens epoxy.
- 3. Through-hole LEDs are incompatible with reflow soldering.
- 4. If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.



○ " Correct mounting method " x " Incorrect mounting method



#### PRECAUTIONARY NOTES

- The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to the latest datasheet for the updated specifications. 2
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