# **Single 2-Input AND Gate**

The NL17SV08 is an ultra-high performance 2–Input AND gate manufactured in 0.35  $\mu$ m technology with excellent performance down to 0.9 volts. This device is ideal for extremely high–speed and high–drive applications. Additionally, limitations of board space are no longer a constraint. The very small SOT–553 makes this device fit most tight designs and spaces.

### Features

- Extremely High Speed:  $t_{PD} = 1.0 \text{ ns} (Typ) @ V_{CC} = 3.3 \text{ V}$
- Designed for 0.9 to 3.3 V Operation
- Overvoltage Tolerance (OVT)\* Input Pins Permit Logic Translation
- Balanced ±24 mA Output Drive @ 3.3 V
- Near Zero Static Supply Current
- Ultra-Tiny SOT-553 5 Pin Package Only 1.6 x 1.6 x 0.6 mm
- These Devices are Pb-Free and are RoHS Compliant

### **Typical Applications**

- Cellular
- Digital Camera
- PDA
- Digital Video

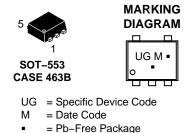
### **Industry Standard**

• Functionally Similar to NC7SV08 and SN74AUC1G08

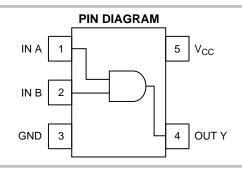


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(Note: Microdot may be in either location)



#### **PIN ASSIGNMENT**

| PIN # | FUNCTION        |  |  |  |  |  |
|-------|-----------------|--|--|--|--|--|
| 1     | IN A            |  |  |  |  |  |
| 2     | IN B            |  |  |  |  |  |
| 3     | GND             |  |  |  |  |  |
| 4     | OUT Y           |  |  |  |  |  |
| 5     | V <sub>CC</sub> |  |  |  |  |  |

#### FUNCTION TABLE

| Input A | Input B | Output Y |
|---------|---------|----------|
| L       | L       | L        |
| L       | Н       | L        |
| Н       | L       | L        |
| Н       | Н       | Н        |

### **ORDERING INFORMATION**

| Device Package |                      | Shipping†                    |
|----------------|----------------------|------------------------------|
| NL17SV08XV5T2G | SOT–553<br>(Pb–Free) | 4000 Tape & Reel<br>(178 mm) |

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*Overvoltage Tolerance (OVT) enables input pins to function outside (higher) of their operating voltages, with no damage to the devices or to signal integrity.

#### MAXIMUM RATINGS

| Symbol           | Rating  | Value                         | Units |  |
|------------------|---|-------------------------------|-------|--|
| V <sub>CC</sub>  | DC Supply Voltage   | -0.5 to +4.6                  | V     |  |
| VI               | DC Input Voltage  | -0.5 to +4.6                  | V     |  |
| Vo               | DC Output Voltage   | –0.5 to V <sub>CC</sub> + 0.5 | V     |  |
| I <sub>IK</sub>  | DC Input Diode Current<br>V <sub>IN</sub> < 0 V   | -50                           | mA    |  |
| I <sub>OK</sub>  | DC Output Diode Current<br>V <sub>OUT</sub> < 0 V<br>V <sub>OUT</sub> > V <sub>CC</sub> | -50<br>+50                    | mA    |  |
| Ι <sub>Ο</sub>   | DC Output Sink Current  | ±50                           | mA    |  |
| I <sub>CC</sub>  | DC Supply Current per Supply Pin  | ±50                           | mA    |  |
| I <sub>GND</sub> | DC Ground Current per Ground Pin  | ±50                           | mA    |  |
| T <sub>STG</sub> | Storage Temperature Range   | -65 to +150                   | °C    |  |
| ΤL               | Lead Temperature, 1.0 mm from Case for 10 seconds                                       | 260                           | °C    |  |
| ТJ               | Junction Temperature Under Bias   | +150                          | °C    |  |
| $\theta_{JA}$    | Thermal Resistance (Note 1)   | 250                           | °C/W  |  |
| PD               | Power Dissipation in Still Air at 85°C  | 250                           | mW    |  |
| MSL              | Moisture Sensitivity  | Level 1                       |       |  |
| F <sub>R</sub>   | Flammability Rating<br>Oxygen Index: 28 to 34   | UL 94 V–0 @ 0.125 in          |       |  |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow.

#### **RECOMMENDED OPERATING CONDITIONS**

| Symbol                           | Parameter   | Min | Max             | Units |
|----------------------------------|---|-----|-----------------|-------|
| V <sub>CC</sub>                  | Positive DC Supply Voltage  | 0.9 | 3.6             | V     |
| V <sub>IN</sub>                  | Digital Input Voltage   | 0   | 3.6             | V     |
| Vout                             | Output Voltage  | 0   | V <sub>CC</sub> | V     |
| I <sub>OH</sub> /I <sub>OL</sub> | $\begin{array}{l} \text{Output Current} \\ \text{V}_{\text{CC}} = 3.0 \ \text{V to } 3.6 \ \text{V} \\ \text{V}_{\text{CC}} = 2.3 \ \text{V to } 2.7 \ \text{V} \\ \text{V}_{\text{CC}} = 1.65 \ \text{V to } 1.95 \ \text{V} \\ \text{V}_{\text{CC}} = 1.4 \ \text{V to } 1.6 \ \text{V} \\ \text{V}_{\text{CC}} = 1.1 \ \text{V to } 1.3 \ \text{V} \\ \text{V}_{\text{CC}} = 0.9 \ \text{V} \end{array}$ |     |                 | mA    |
| t <sub>A</sub>                   | Operating Temperature Range. All Package Types  | -40 | +85             | °C    |
| t <sub>r</sub> , t <sub>f</sub>  | Input Rise or Fall Time $V_{CC} = 3.3 V \pm 0.3 V$  | 0   | 10              | nS/V  |

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

#### **DEVICE JUNCTION TEMPERATURE VERSUS TIME TO 0.1% BOND FAILURES**

| Junction Temperature °C | Time, Hours | Time, Years |
|-------------------------|-------------|-------------|
| 80                      | 1,032,200   | 117.8       |
| 90                      | 419,300     | 47.9        |
| 100                     | 178,700     | 20.4        |
| 110                     | 79,600      | 9.4         |
| 120                     | 37,000      | 4.2         |
| 130                     | 17,800      | 2.0         |
| 140                     | 8,900       | 1.0         |

## NL17SV08

#### DC CHARACTERISTICS – Digital Section (Voltages Referenced to GND)

|                  |                              |                            |   | T <sub>A</sub> =  | 25°C   | $T_A = -40$   | ) to 85°C  |       |
|------------------|------------------------------|----------------------------|---|---|--|---|--|-------|
| Symbol           | Parameter                    | Condition                  | V <sub>CC</sub>   | Min   | Max  | Min   | Max  | Units |
| V <sub>IH</sub>  | High Level<br>Input Voltage  |                            | $\begin{array}{c} 0.90\\ 1.10 \leq V_{CC} \leq 1.30\\ 1.40 \leq V_{CC} \leq 1.60\\ 1.65 \leq V_{CC} \leq 1.95\\ 2.30 \leq V_{CC} \leq 2.70\\ 2.70 \leq V_{CC} \leq 3.60 \end{array}$      | $\begin{array}{c} 0.65 \ x \ V_{CC} \\ 1.6 \\ 2.0 \end{array}$               |  | $\begin{array}{c} 0.65 \times V_{CC} \\ 0.65 \times V_{CC} \\ 0.65 \times V_{CC} \\ 0.65 \times V_{CC} \\ 1.6 \\ 2.0 \end{array}$           |  | V     |
| VIL              | Low Level<br>Input Voltage   |                            | $\begin{array}{c} 0.90\\ 1.10 \leq V_{CC} \leq 1.30\\ 1.40 \leq V_{CC} \leq 1.60\\ 1.65 \leq V_{CC} \leq 1.95\\ 2.30 \leq V_{CC} \leq 2.70\\ 2.70 \leq V_{CC} \leq 3.60 \end{array}$      |   | 0.35 x V <sub>CC</sub><br>0.35 x V <sub>CC</sub><br>0.35 x V <sub>CC</sub><br>0.35 x V <sub>CC</sub><br>0.7<br>0.8 |   | 0.35 x V <sub>CC</sub><br>0.35 x V <sub>CC</sub><br>0.35 x V <sub>CC</sub><br>0.35 x V <sub>CC</sub><br>0.7<br>0.8 | V     |
| V <sub>OH</sub>  | High Level<br>Output Voltage | I <sub>OH</sub> = -100 μA  | $\begin{array}{c} 0.90\\ 1.10 \leq V_{CC} \leq 1.30\\ 1.40 \leq V_{CC} \leq 1.60\\ 1.65 \leq V_{CC} \leq 1.95\\ 2.30 \leq V_{CC} \leq 2.70\\ 2.70 \leq V_{CC} \leq 3.60 \end{array}$      | $\begin{array}{c} V_{CC} - 0.1 \\ V_{CC} - 0.1 \\ V_{CC} - 0.2 \end{array}$ |  | $\begin{array}{c} V_{CC} - 0.1 \\ V_{CC} - 0.1 \\ V_{CC} - 0.2 \end{array}$ |  | V     |
|                  |                              | I <sub>OH</sub> = -2.0 mA  | $1.10 \leq V_{CC} \leq 1.30$  | 0.75 x V <sub>CC</sub>  |  | 0.75 x V <sub>CC</sub>  |  |       |
|                  |                              | I <sub>OH</sub> = -4.0 mA  | $1.40 \leq V_{CC} \leq 1.60$  | 0.75 x V <sub>CC</sub>  |  | 0.75 x V <sub>CC</sub>  |  |       |
|                  |                              | I <sub>OH</sub> = -6.0 mA  | $\begin{array}{l} 1.65 \leq V_{CC} \leq 1.95 \\ 2.30 \leq V_{CC} \leq 2.70 \end{array}$   | 1.25<br>2.0   |  | 1.25<br>2.0   |  |       |
|                  |                              | I <sub>OH</sub> = -12 mA   | $\begin{array}{c} 2.30 \leq V_{CC} \leq 2.70 \\ 2.70 < V_{CC} \leq 3.60 \end{array}$  | 1.8<br>2.2  |  | 1.8<br>2.2  |  |       |
|                  |                              | I <sub>OH</sub> = -18 mA   | $\begin{array}{c} 2.30 \leq V_{CC} \leq 2.70 \\ 2.70 < V_{CC} \leq 3.60 \end{array}$  | 1.7<br>2.4  |  | 1.7<br>2.4  |  |       |
|                  |                              | I <sub>OH</sub> = -24 mA   | $2.70 \leq V_{CC} \leq 3.60$  | 2.2   |  | 2.2   |  |       |
| V <sub>OL</sub>  | Low Level<br>Output Voltage  | I <sub>OL</sub> = 100 μA   | $\begin{array}{c} 0.90 \\ 1.10 \leq V_{CC} \leq 1.30 \\ 1.40 \leq V_{CC} \leq 1.60 \\ 1.65 \leq V_{CC} \leq 1.95 \\ 2.30 \leq V_{CC} \leq 2.70 \\ 2.70 \leq V_{CC} \leq 3.60 \end{array}$ |   | 0.1<br>0.2<br>0.2<br>0.2<br>0.2<br>0.2   |   | 0.1<br>0.2<br>0.2<br>0.2<br>0.2<br>0.2   | V     |
|                  |                              | I <sub>OL</sub> = 2.0 mA   | $1.10 \leq V_{CC} \leq 1.30$  |   | 0.25 x V <sub>CC</sub>   |   | 0.25 x V <sub>CC</sub>   |       |
|                  |                              | I <sub>OL</sub> = 4.0 mA   | $1.40 \le V_{CC} \le 1.60$  |   | $0.25 \times V_{CC}$   |   | 0.25 x V <sub>CC</sub>   |       |
|                  |                              | I <sub>OL</sub> = 6.0 mA   | $1.65 \leq V_{CC} \leq 1.95$  |   | 0.3  |   | 0.3  |       |
|                  |                              | I <sub>OL</sub> = 12 mA    | $2.30 \le V_{CC} \le 2.70$<br>$2.70 < V_{CC} \le 3.60$  |   | 0.4<br>0.4   |   | 0.4<br>0.4   |       |
|                  |                              | I <sub>OL</sub> = 18 mA    | $\begin{array}{l} 2.30 \leq V_{CC} \leq 2.70 \\ 2.70 < V_{CC} \leq 3.60 \end{array}$  |   | 0.6<br>0.4   |   | 0.6<br>0.4   |       |
|                  |                              | I <sub>OL</sub> = 24 mA    | $2.70 \leq V_{CC} \leq 3.60$  |   | 0.55   |   | 0.55   |       |
| I <sub>IN</sub>  | Input<br>Leakage Current     | 0 = V <sub>I</sub> = 3.6 V | 0.90 to 3.60  |   | ±0.1   |   | ±0.9   | μΑ    |
| I <sub>OFF</sub> | Power Off<br>Leakage Current |                            | 0   |   | 10   |   | 10   | μΑ    |
| I <sub>CC</sub>  | Quiescent<br>Supply Current  | $V_{I} = V_{CC}$ or GND    | 0.90 to 3.60  |   | 0.9  |   | 5  | μΑ    |

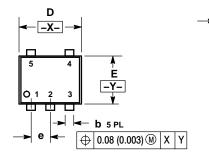
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

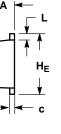
### **AC CHARACTERISTICS** (Input $t_r = t_f = 3.0 \text{ nS}$ )

|                   |                                  |   |   | T <sub>A</sub> = 25°C |                   | T <sub>A</sub> =<br>−40 to 85°C |                   |                   |       |
|-------------------|----------------------------------|---|---|-----------------------|-------------------|---------------------------------|-------------------|-------------------|-------|
| Symbol            | Parameter                        | Condition   | V <sub>cc</sub>   | Min                   | Тур               | Max                             | Min               | Max               | Units |
| T <sub>PHL,</sub> | Propagation Delay                | $C_L$ = 15 pF, $R_L$ = 1.0 M $\Omega$                 | 0.90  |                       | 13                |                                 |                   |                   | nS    |
| T <sub>PLH</sub>  |                                  | $C_L$ = 15 pF, $R_L$ = 2.0 k $\Omega$                 | $\begin{array}{l} 1.10 \leq V_{CC} \leq 1.30 \\ 1.40 \leq V_{CC} \leq 1.60 \end{array}$                               | 3.0<br>1.0            | 6.0<br>3.2        | 10.0<br>6.0                     | 1.0<br>1.0        | 14.6<br>7.2       | nS    |
|                   |                                  | $C_L$ = 30 pF, $R_L$ = 500 $\Omega$                   | $\begin{array}{c} 1.65 \leq V_{CC} \leq 1.95 \\ 2.30 \leq V_{CC} \leq 2.70 \\ 2.70 \leq V_{CC} \leq 3.60 \end{array}$ | 1.0<br>0.8<br>0.7     | 2.0<br>1.2<br>1.0 | 4.5<br>2.6<br>2.3               | 1.0<br>0.7<br>0.6 | 5.3<br>3.7<br>3.0 | nS    |
| C <sub>IN</sub>   | Input Capacitance                |   | 0   |                       | 2.0               |                                 |                   |                   | pF    |
| C <sub>OUT</sub>  | Output Capacitance               |   | 0   |                       | 4.5               |                                 |                   |                   | pF    |
| C <sub>PD</sub>   | Power Dissipation<br>Capacitance | V <sub>I</sub> = 0 V or V <sub>CC</sub><br>F = 10 MHz | 0.90 to 3.60  |                       | 20                |                                 |                   |                   | pF    |

#### PACKAGE DIMENSIONS

SOT-553, 5 LEAD CASE 463B ISSUE C





NOTES:

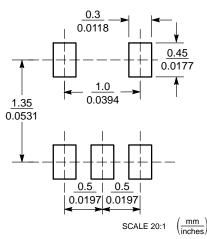
DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
CONTROLLING DIMENSION: MILLIMETERS

2. CONTROLLING DIMENSION: MILLIMETERS 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH

THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

|     | м    | ILLIMETE           | RS   |       | INCHES |       |
|-----|------|--------------------|------|-------|--------|-------|
| DIM | MIN  | NOM                | MAX  | MIN   | NOM    | MAX   |
| Α   | 0.50 | 0.55               | 0.60 | 0.020 | 0.022  | 0.024 |
| b   | 0.17 | 0.22               | 0.27 | 0.007 | 0.009  | 0.011 |
| c   | 0.08 | 0.13               | 0.18 | 0.003 | 0.005  | 0.007 |
| D   | 1.55 | 1.60               | 1.65 | 0.061 | 0.063  | 0.065 |
| Е   | 1.15 | 1.20               | 1.25 | 0.045 | 0.047  | 0.049 |
| е   |      | 0.50 BSC 0.020 BSC |      |       |        |       |
| L   | 0.10 | 0.20               | 0.30 | 0.004 | 0.008  | 0.012 |
| HE  | 1.55 | 1.60               | 1.65 | 0.061 | 0.063  | 0.065 |

RECOMMENDED SOLDERING FOOTPRINT\*



\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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