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FAIRCHILD SEMICONDUCTOR®

January 2014

FQD12N20L

N-Channel QFET® MOSFET

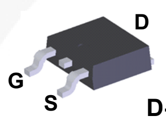
200 V, 9.0 A, 280 mΩ

Description

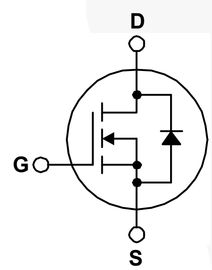
This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

Features

- 9.0 A, 200 V, $R_{DS(on)}$ = 280 mΩ (Max.) @ V_{GS} = 10 V, I_D = 4.5 A
- Low Gate Charge (Typ. 16 nC)
- Low Crss (Typ. 17 pF)
- 100% Avalanche Tested



D-PAK



Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter		FQD12N20LTM	Unit
V_{DSS}	Drain-Source Voltage		200	V
I_D	Drain Current - Continuous (T_C = 25°C)		9.0	A
	- Continuous (T_C = 100°C)		5.7	A
I_{DM}	Drain Current - Pulsed	(Note 1)	36	A
V_{GSS}	Gate-Source Voltage		± 20	V
E_{AS}	Single Pulsed Avalanche Energy	(Note 2)	210	mJ
I_{AR}	Avalanche Current	(Note 1)	9.0	A
E_{AR}	Repetitive Avalanche Energy	(Note 1)	5.5	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	5.5	V/ns
P_D	Power Dissipation (T_A = 25°C) *		2.5	W
	Power Dissipation (T_C = 25°C)		55	W
	- Derate above 25°C		0.44	W/°C
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	°C
T_L	Maximum lead temperature for soldering, 1/8" from case for 5 seconds		300	°C

Thermal Characteristics

Symbol	Parameter	FQD12N20LTM	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	2.27	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Minimum Pad of 2-oz Copper), Max.	110	
	Thermal Resistance, Junction to Ambient (*1 in^2 Pad of 2-oz Copper), Max.	50	

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQD12N20LTM	FQD12N20L	DPAK	Tape and Reel	330 mm	16 mm	2500 units

Electrical Characteristics

T_C = 25°C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS} = 0 V, I_D = 250 μA	200	--	--	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	I_D = 250 μA, Referenced to 25°C	--	0.14	--	V/°C
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 200 V, V_{GS} = 0 V	--	--	1	μA
		V_{DS} = 160 V, T_C = 125°C	--	--	10	μA
I_{GSSF}	Gate-Body Leakage Current, Forward	V_{GS} = 20 V, V_{DS} = 0 V	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	V_{GS} = -20 V, V_{DS} = 0 V	--	--	-100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250 μA	1.0	--	2.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	V_{GS} = 10 V, I_D = 4.5 A V_{GS} = 5 V, I_D = 4.5 A	--	0.22 0.25	0.28 0.32	Ω
g_{FS}	Forward Transconductance	V_{DS} = 30 V, I_D = 4.5 A	--	11.6	--	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS} = 25 V, V_{GS} = 0 V, f = 1.0 MHz	--	830	1080	pF
C_{oss}	Output Capacitance		--	120	155	pF
C_{rss}	Reverse Transfer Capacitance		--	17	22	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	V_{DD} = 100 V, I_D = 11.6 A, R_G = 25 Ω (Note 4)	--	15	40	ns
t_r	Turn-On Rise Time		--	190	390	ns
$t_{d(off)}$	Turn-Off Delay Time		--	60	130	ns
t_f	Turn-Off Fall Time		--	120	250	ns
Q_g	Total Gate Charge	V_{DS} = 160 V, I_D = 11.6 A, V_{GS} = 5 V (Note 4)	--	16	21	nC
Q_{gs}	Gate-Source Charge		--	2.8	--	nC
Q_{gd}	Gate-Drain Charge		--	7.6	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain-Source Diode Forward Current		--	--	9.0	A
I_{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	36	A
V_{SD}	Drain-Source Diode Forward Voltage	V_{GS} = 0 V, I_S = 9.0 A	--	--	1.5	V
t_{rr}	Reverse Recovery Time	V_{GS} = 0 V, I_S = 11.6 A, dI_F / dt = 100 A/μs	--	128	--	ns
Q_{rr}	Reverse Recovery Charge		--	0.56	--	μC

Notes:
1. Repetitive rating : pulse width limited by maximum junction temperature.
2. L = 3.9 mH, I_{AS} = 9.0 A, V_{DD} = 50 V, R_G = 25 Ω, starting T_J = 25°C.
3. $I_{SD} \leq$ 11.6 A, di/dt $\leq$ 300 A/μs, $V_{DD} \leq BV_{DSS}$, starting T_J = 25°C.
4. Essentially independent of operating temperature.

Typical Characteristics

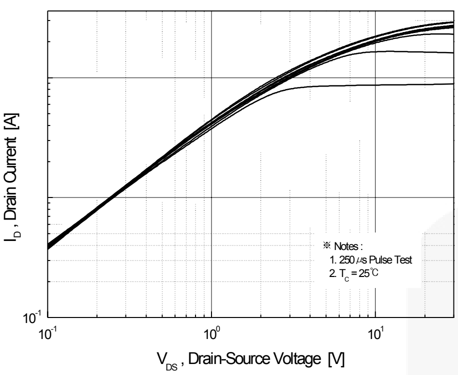


Figure 1. On-Region Characteristics

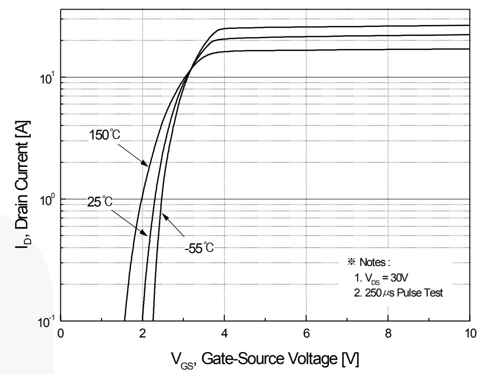


Figure 2. Transfer Characteristics

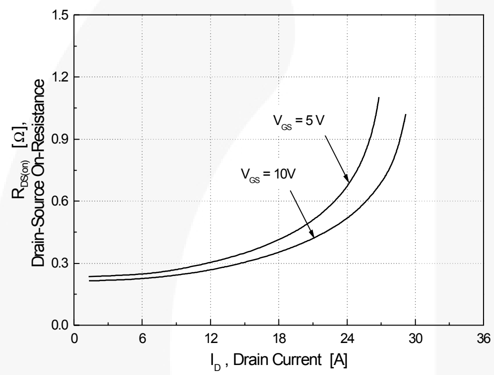


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

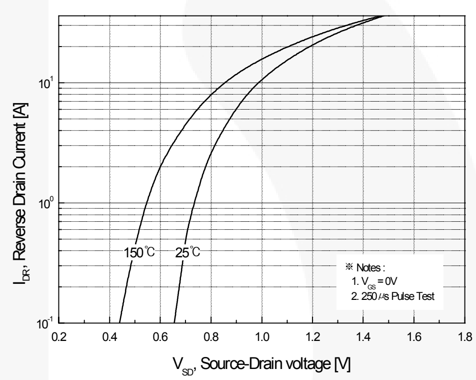


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

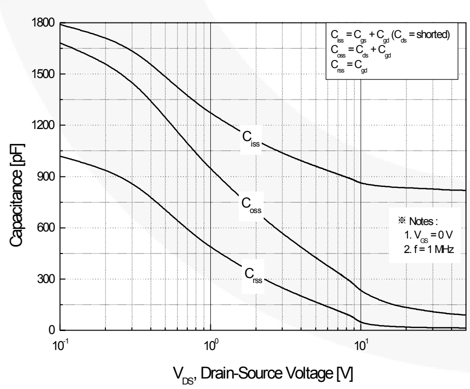


Figure 5. Capacitance Characteristics

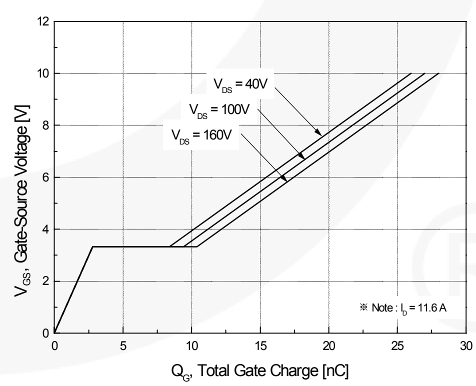


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

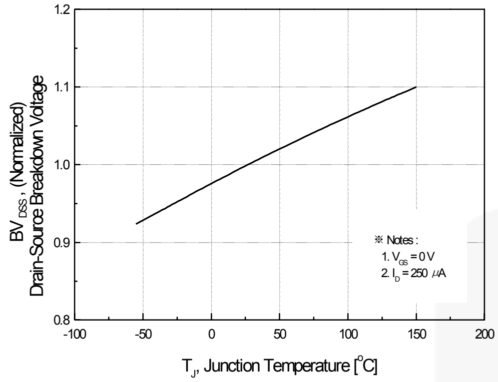


Figure 7. Breakdown Voltage Variation vs. Temperature

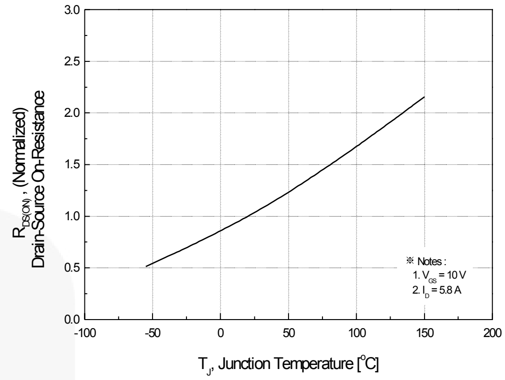


Figure 8. On-Resistance Variation vs. Temperature

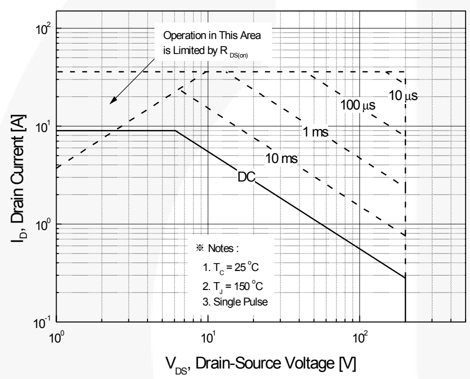


Figure 9. Maximum Safe Operating Area

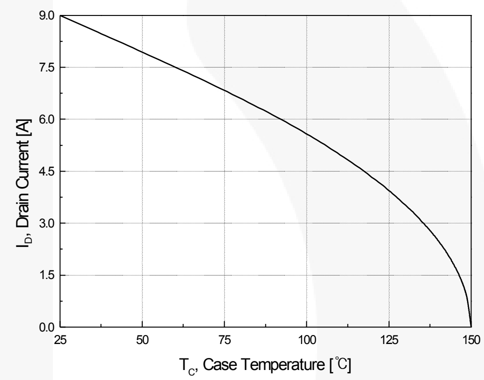


Figure 10. Maximum Drain Current vs. Case Temperature

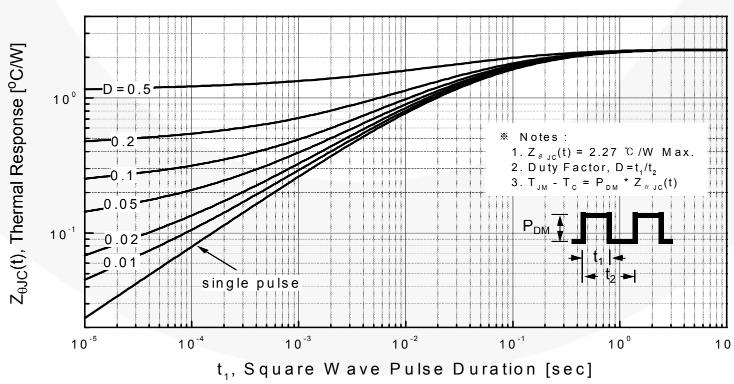


Figure 11. Transient Thermal Response Curve

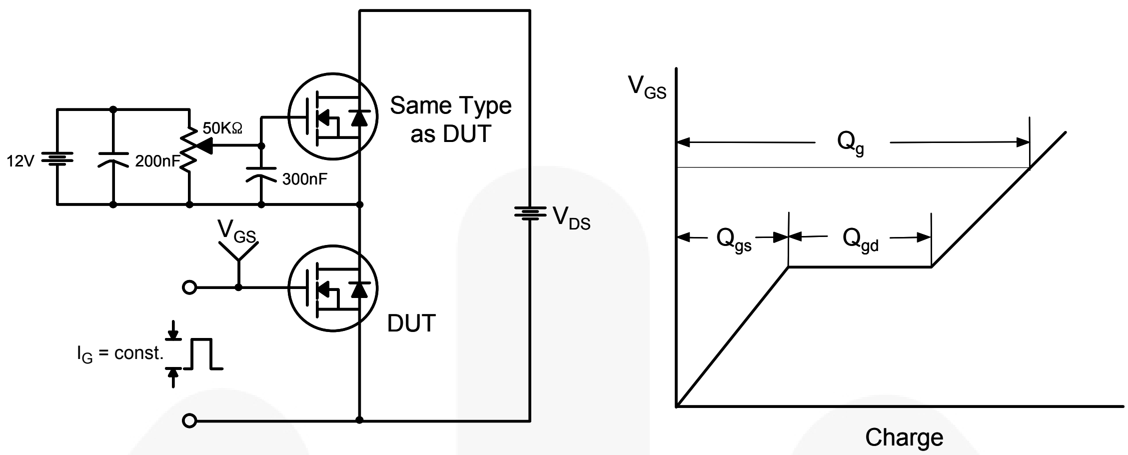


Figure 12. Gate Charge Test Circuit & Waveform

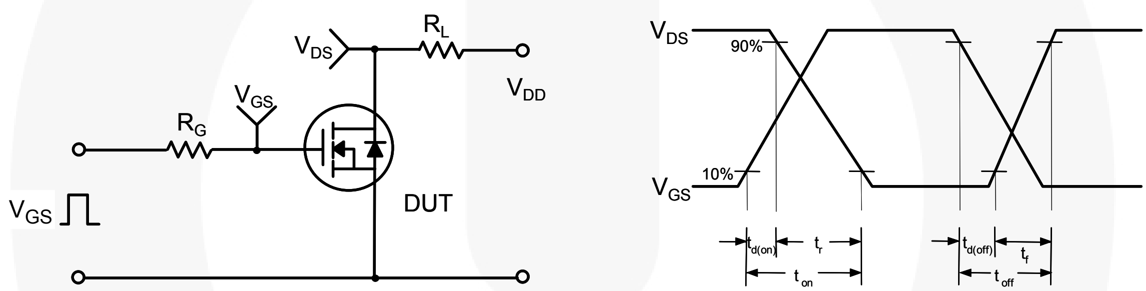


Figure 13. Resistive Switching Test Circuit & Waveforms

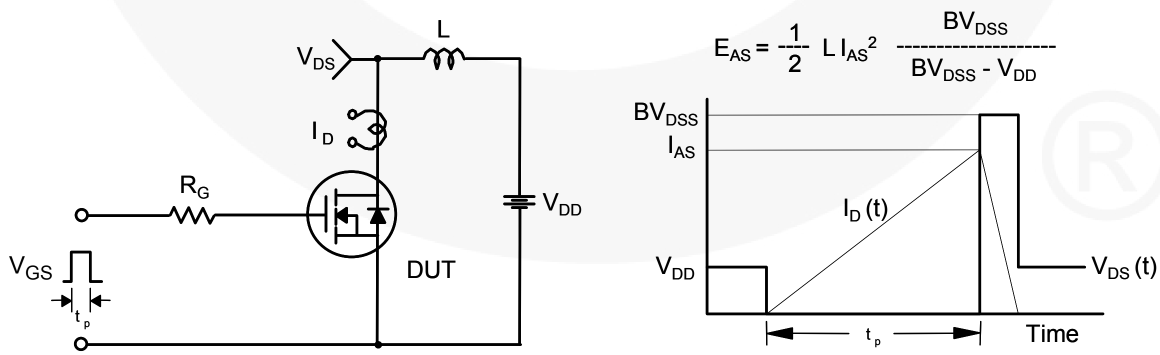


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

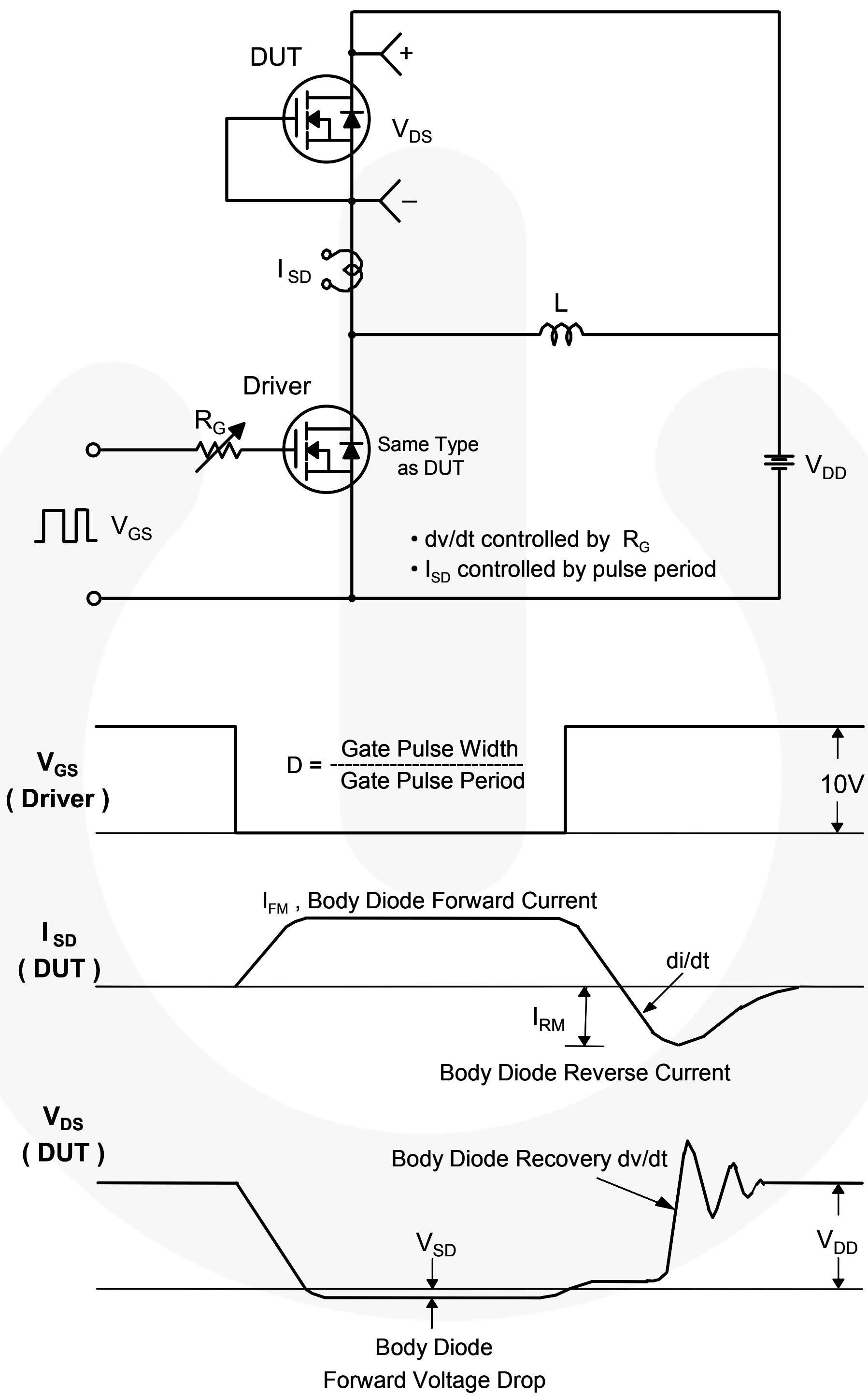


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Mechanical Dimensions

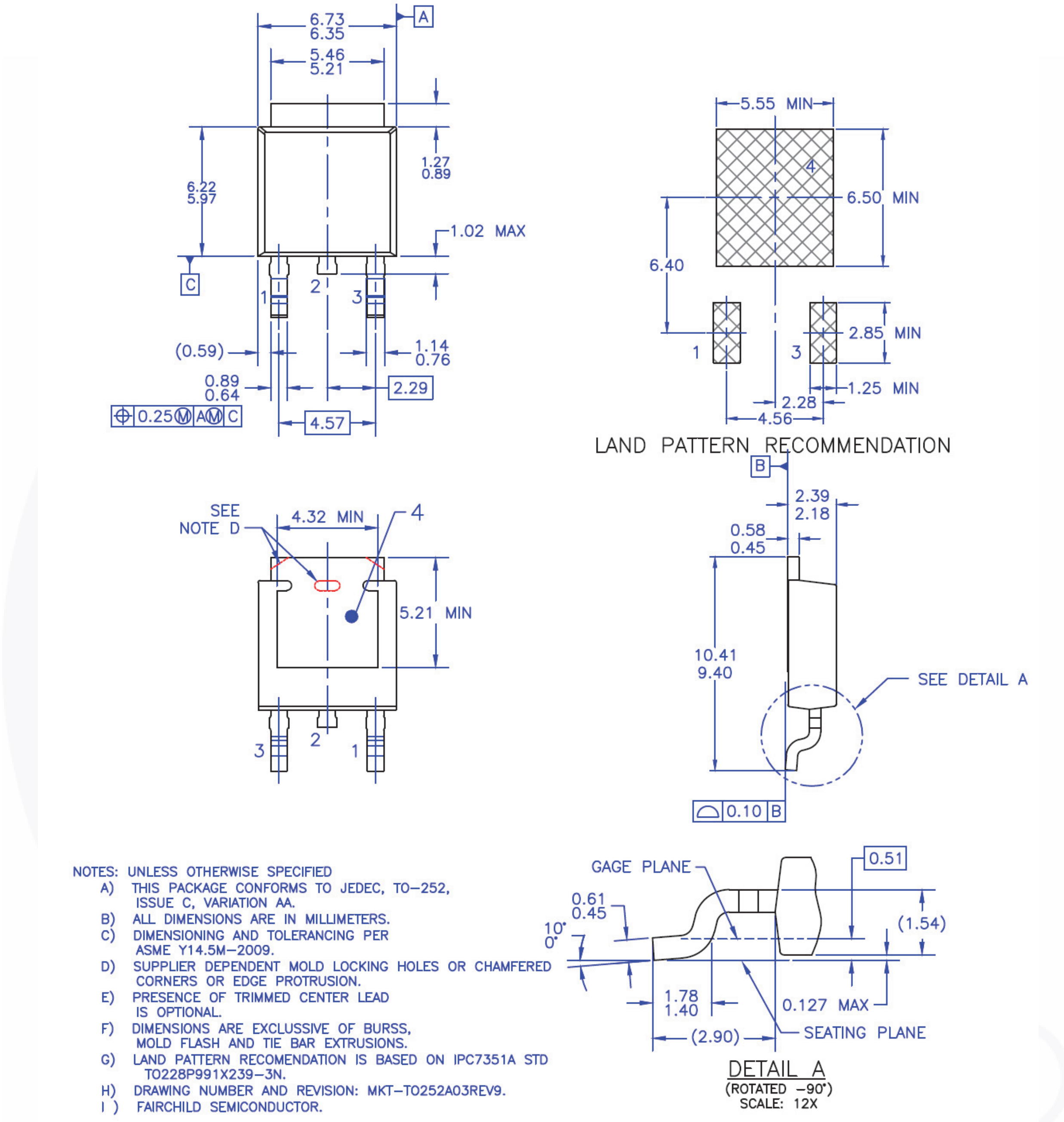


Figure 16. TO252 (D-PAK), Molded, 3-Lead, Option AA&AB

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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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