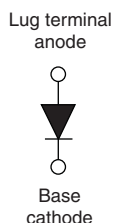



# High Performance Schottky Rectifier, 120 A


**HALF-PAK (D-67)**


## FEATURES

- 150 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for industrial level
- UL approved file E222165 
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?999912](http://www.vishay.com/doc?999912)


**RoHS**  
COMPLIANT

## PRIMARY CHARACTERISTICS

I <sub>F(AV)</sub>	120 A
V <sub>R</sub>	30 V
Package	HALF-PAK (D-67)
Circuit configuration	Single diode

## DESCRIPTION

The VS-122NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

## MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
I <sub>F(AV)</sub>	Rectangular waveform	120	A
V <sub>RRM</sub>		30	V
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	18 000	A
V <sub>F</sub>	120 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.47	V
T <sub>J</sub>	Range	-55 to +150	°C

## VOLTAGE RATINGS

PARAMETER	SYMBOL	VS-122NQ030PbF	UNITS
Maximum DC reverse voltage	V <sub>R</sub>	30	V
Maximum working peak reverse voltage	V <sub>RWM</sub>		

## ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 115 °C, rectangular waveform	120	A
Maximum peak one cycle non-repetitive surge current See fig. 7	I <sub>FSM</sub>	5 μs sine or 3 μs rect. pulse	18 000	A
		10 ms sine or 6 ms rect. pulse	2000	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 11 A, L = 1 mH	54	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 μs Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical	12	A

**ELECTRICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	$V_{FM}^{(1)}$	120 A	0.57	V
		240 A	0.75	
		120 A	0.47	
		240 A	0.67	
Maximum reverse leakage current per leg See fig. 2	$I_{RM}^{(1)}$	$T_J = 25\text{ }^{\circ}\text{C}$	10	mA
		$T_J = 125\text{ }^{\circ}\text{C}$	560	
Maximum junction capacitance	$C_T$	$V_R = 5\text{ V}_{DC}$ (test signal range 100 kHz to 1 MHz) $25\text{ }^{\circ}\text{C}$	7400	pF
Typical series inductance	$L_S$	From top of terminal hole to mounting plane	7.0	nH
Maximum voltage rate of change	dV/dt	Rated $V_R$	10 000	V/ $\mu$ s

**Note**(1) Pulse width < 300  $\mu$ s, duty cycle < 2 %**THERMAL - MECHANICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55 to 150	°C
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	DC operation See fig. 4	0.38	°C/W
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.05	
Approximate weight			30	g
			1.06	oz.
Mounting torque	minimum	Non-lubricated threads	3 (26.5)	N · m (lbf · in)
	maximum		4 (35.4)	
Terminal torque	minimum		3.4 (30)	
	maximum		5 (44.2)	
Case style			HALF-PAK module	

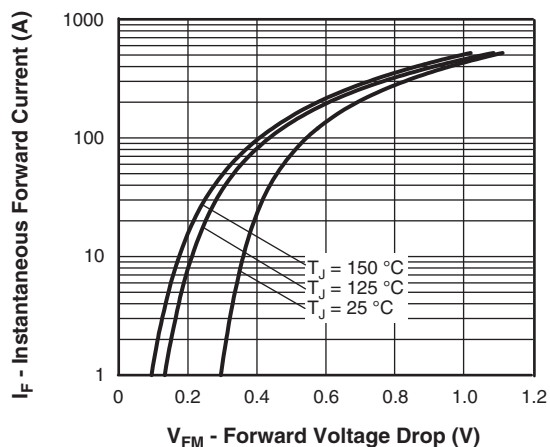


Fig. 1 - Maximum Forward Voltage Drop Characteristics

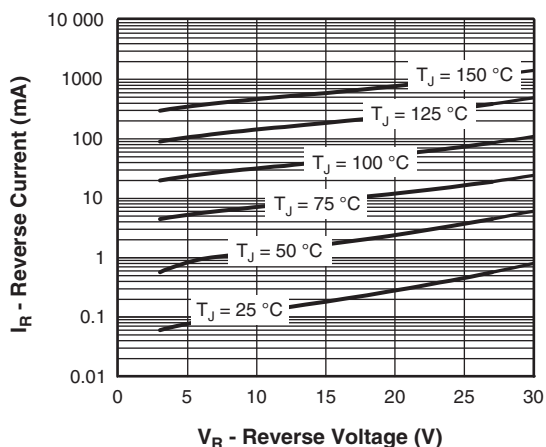


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

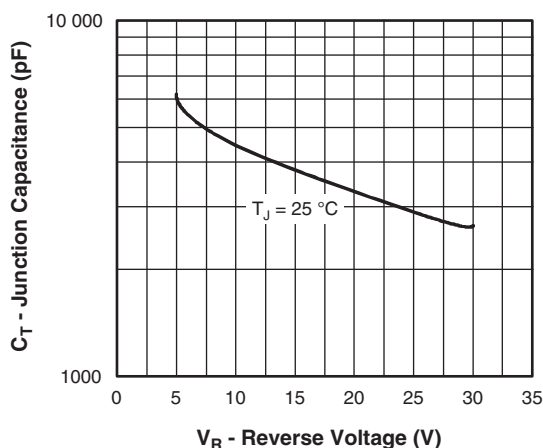


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

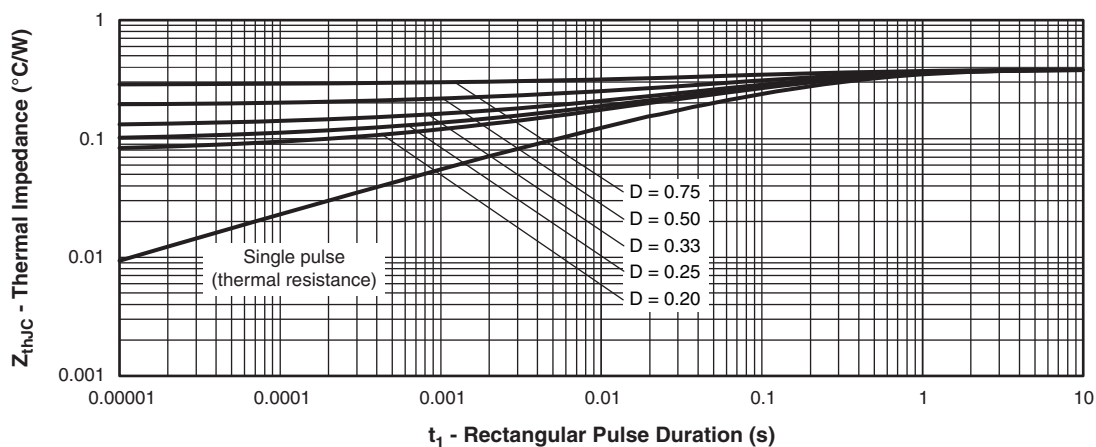


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

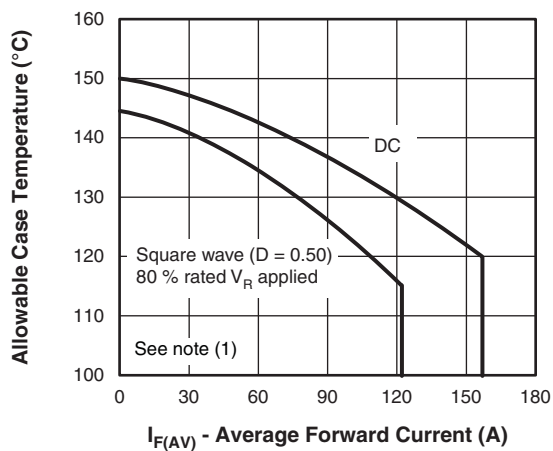


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

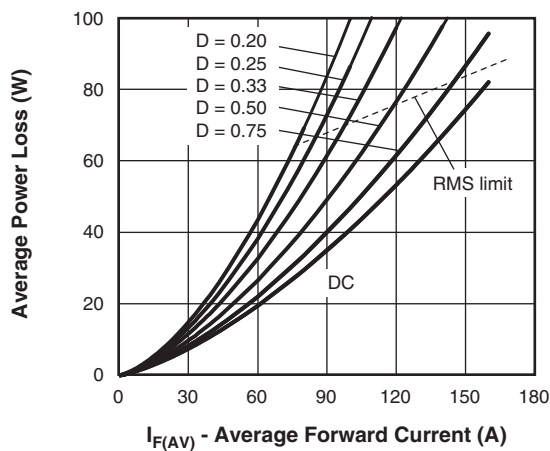


Fig. 6 - Forward Power Loss Characteristics

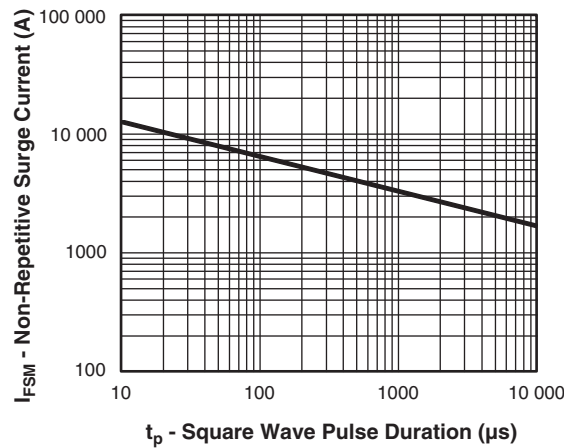


Fig. 7 - Maximum Non-Repetitive Surge Current

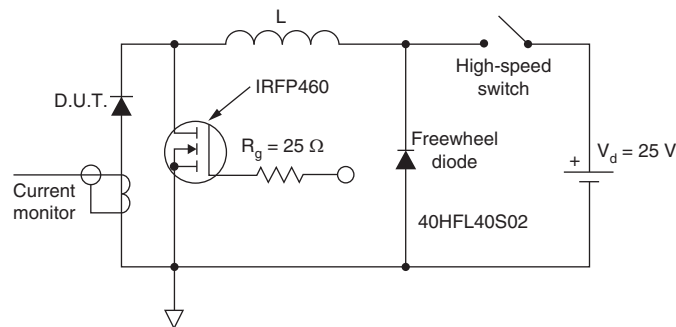


Fig. 8 - Unclamped Inductive Test Circuit

### Note

- (1) Formula used:  $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$ ;  
 $P_d$  = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  
 $P_{dREV}$  = inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1}$  = rated  $V_R$

## ORDERING INFORMATION TABLE

Device code	VS-	12	2	N	Q	030	PbF
	1	2	3	4	5	6	7
1	Vishay Semiconductors product						
2	Average current rating (x 10)						
3	Product silicon identification						
4	N = not isolated						
5	Q = Schottky rectifier diode						
6	Voltage rating (030 = 30 V)						
7	Lead (Pb)-free						

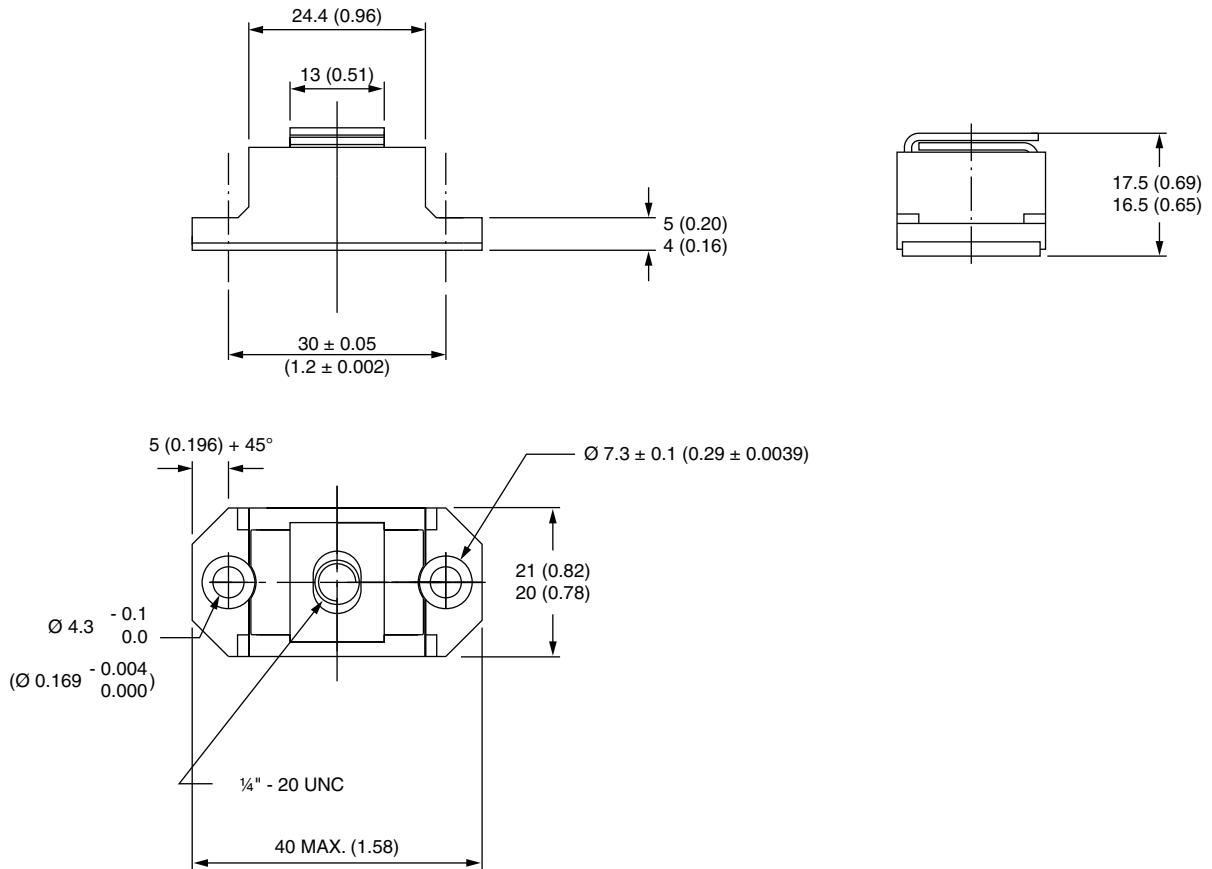
### LINKS TO RELATED DOCUMENTS

Dimensions

[www.vishay.com/doc?95020](http://www.vishay.com/doc?95020)

## D-67 HALF-PAK

**DIMENSIONS** in millimeters (inches)





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