



100V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON) max}	I _D T _A = +25°C	
4001/	$220\text{m}\Omega$ @ $V_{GS} = 10V$	2.3A	
100V	$250 \text{m}\Omega$ @ $V_{GS} = 4.5 \text{V}$	2.1A	

Description

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- DC-DC Converters
- Power Management Functions

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- · Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DMN10H220LEQ</u>)

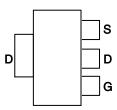
Mechanical Data

- Case: SOT223
- Case Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Leadframe;
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.112 grams (Approximate)

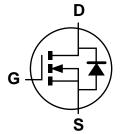




Top View



Pin Out - Top View



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
DMN10H220LE-13	Standard	SOT223	2,500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

☐ ☐ YWW 10H220

SOT223

O'!! = Manufacturer's Marking 10H220 = Marking Code YWW = Date Code Marking Y or Y= Year (ex: 7 = 2017) WW = Week (01 to 53)



Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		V _{DSS}	100	V
Gate-Source Voltage	V_{GSS}	±20	V	
Continuous Drain Current (Note 5) // 40/	$T_A = +25$ °C $T_A = +70$ °C	I _D	2.3 1.8	А
Continuous Drain Current (Note 5) V _{GS} = 10V	$T_C = +25$ °C $T_C = +70$ °C	I _D	6.2 4.9	А
Maximum Continuous Body Diode Forward Current (Note 5)	Is	1.5	A	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	8	A

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	TA = +25°C	P _D	1.8	W
Total Power Dissipation (Note 5)	Ta = +70°C		1.1	
Thermal Resistance, Junction to Ambient (Note 5)		$R_{\theta JA}$	69	°C/W
Total Power Dissipation (Note 5) $Tc = +25^{\circ}C$		P _D	14	W
Thermal Resistance, Junction to Case (Note 5)		$R_{\theta JC}$	8.7	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@ $T_A = \pm 25$ °C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV _{DSS}	100	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μΑ	V _{DS} = 100V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 16V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)				•	•		
Gate Threshold Voltage	V _{GS(TH)}	1	1.7	2.5	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance	D-s/s/s	_	155	220	mΩ	$V_{GS} = 10V, I_D = 1.6A$	
Static Dialif-Source Off-Resistance	R _{DS(ON)}	_	190	250	11122	$V_{GS} = 4.5V, I_D = 1.3A$	
Diode Forward Voltage	V_{SD}	_	0.8	1.5	V	$V_{GS} = 0V, I_{S} = 1.1A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	C _{iss}	_	401	_		$V_{DS} = 25V, V_{GS} = 0V$ f = 1.0MHz	
Output Capacitance	Coss	_	22	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	17	_			
Gate Resistance	R_g	_	2.1	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Q_g	_	4.1	_			
Total Gate Charge (V _{GS} = 10V)	Qg	_	8.3	_	nC	V _{DS} = 50V. I _D = 1.6A	
Gate-Source Charge	Q _{gs}	_	1.5	_	IIC	VDS = 50V, ID = 1.0A	
Gate-Drain Charge	Q_{gd}	_	2	_			
Turn-On Delay Time	t _{D(ON)}	_	6.8	_			
Turn-On Rise Time	t _R	_	8.2	_		$V_{DS} = 50V$, $V_{GS} = 4.5V$, $R_G = 6.8\Omega$, $I_D = 1.0A$	
Turn-Off Delay Time	t _{D(OFF)}	_	7.9	_	ns		
Turn-Off Fall Time	t _F	_	3.6	_			
Reverse Recovery Time	t _{RR}	_	17	_	ns	L- 1.10 di/dt 1000/up	
Reverse Recovery Charge	Q _{RR}		9.8	_	nC	$I_S = 1.1A$, di/dt =100A/ μ s	

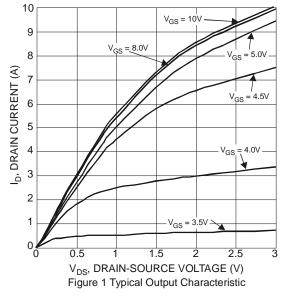
Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1-inch square copper plate.

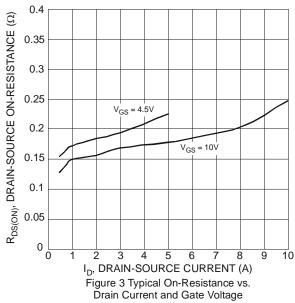
^{6.} Short duration pulse test used to minimize self-heating effect.

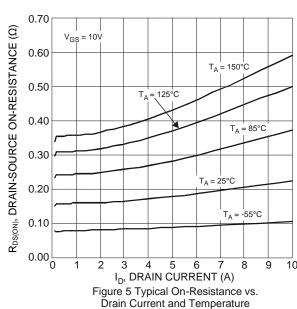
^{7.} Guaranteed by design. Not subject to production testing.

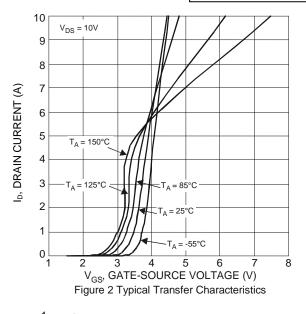


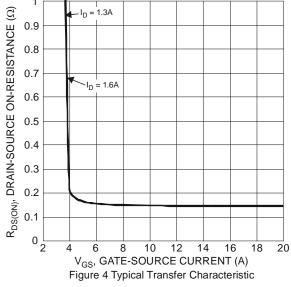












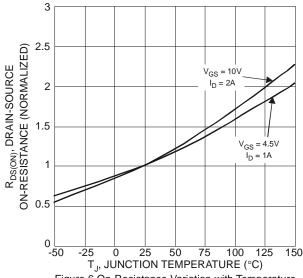
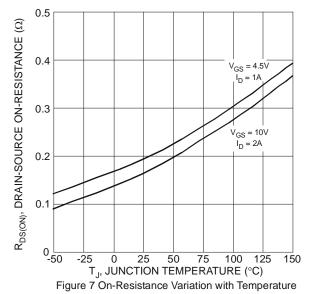


Figure 6 On-Resistance Variation with Temperature







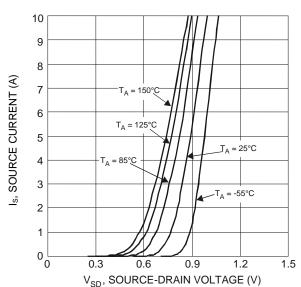
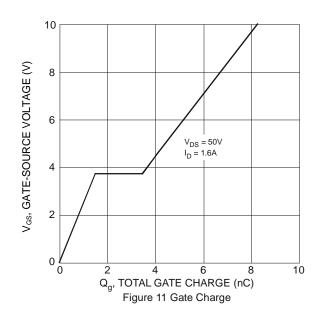


Figure 9 Diode Forward Voltage vs. Current



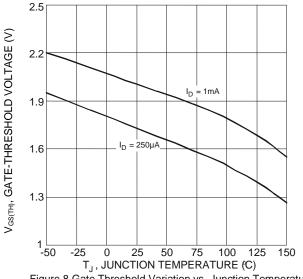
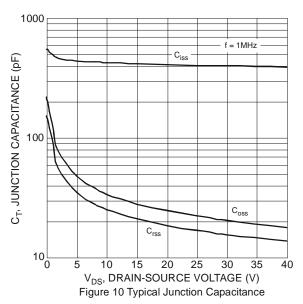
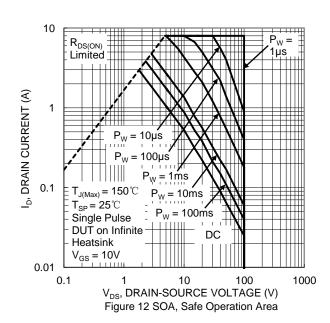
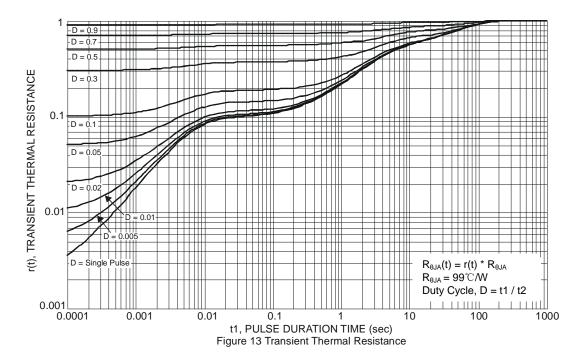


Figure 8 Gate Threshold Variation vs. Junction Temperature







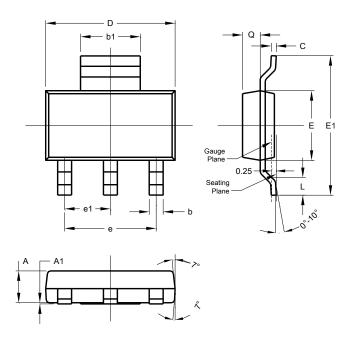




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT223

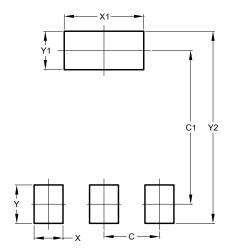


SOT223					
Dim	Min	Max	Тур		
Α	1.55	1.65	1.60		
A1	0.010	0.15	0.05		
b	0.60	0.80	0.70		
b1	2.90	3.10	3.00		
С	0.20	0.30	0.25		
D	6.45	6.55	6.50		
Е	3.45	3.55	3.50		
E1	6.90	7.10	7.00		
е	-	-	4.60		
e1	-	-	2.30		
L	0.85	1.05	0.95		
Q	0.84	0.94	0.89		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT223



Dimensions	Value (in mm)		
С	2.30		
C1	6.40		
Х	1.20		
X1	3.30		
Υ	1.60		
Y1	1.60		
Y2	8.00		



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