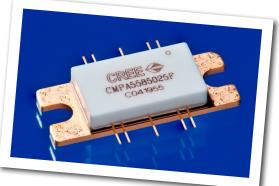


CMPA5585025F

25 W, 5.5 - 8.5 GHz, GaN MMIC, Power Amplifier

Cree's CMPA5585025F is a gallium nitride (GaN) High Electron Mobility Transistor (HEMT) based monolithic microwave integrated circuit (MMIC). GaN has superior properties compared to silicon or gallium arsenide, including higher breakdown voltage, higher saturated electron drift velocity and higher thermal conductivity. GaN HEMTs also offer greater power density and wider bandwidths compared to Si and GaAs transistors. This MMIC is available in a 10 lead metal/ceramic flanged package for optimal electrical and thermal performance.



PN: CMPA5585025F Package Type: 440213

Typical Performance Over 5.8-8.4 GHz (T_c = 25°C)

Parameter	5.8 GHz	6.4 GHz	7.2 GHz	7.9 GHz	8.4 GHz	Units
Small Signal Gain	29.5	24.0	24.0	24.0	22.0	dB
Output Power ¹	15	23	20	19	19	W
Power Gain ¹	21.7	19.5	17.2	18.5	18.6	dB
Power Added Efficiency ¹	30	25	20.5	19	19.5	%

Note¹: Measured at -30 dBc, 1.6 MHz from carrier, in the CMPA5585025F-AMP under OQPSK modulation, 1.6 Msps, PN23, Alpha Filter = 0.2.

Features

- 25 dB Small Signal Gain
- 35 W Typical P_{SAT}
- Operation up to 28 V
- High Breakdown Voltage
- High Temperature Operation
- Size 1.00 x 0.385 inches

Applications

- Point to Point Radio
- Communications
- Satellite Communication Uplink



Absolute Maximum Ratings (not simultaneous)

Parameter	Symbol	Rating	Units	Conditions
Drain-source Voltage	V _{DSS}	84	V _{DC}	25°C
Gate-source Voltage	V _{gs}	-10, +2	V _{DC}	25°C
Power Dissipation	P _{DISS}	55	W	
Storage Temperature	T _{stg}	-65, +150	°C	
Operating Junction Temperature	TJ	225	°C	
Maximum Forward Gate Current	I _{GMAX}	10	mA	25°C
Soldering Temperature ¹	Τ _s	245	°C	
Screw Torque	τ	40	in-oz	
Thermal Resistance, Junction to Case	R _{eJC}	1.55	°C/W	0QPSK, 85°C, P _{DISS} = 55 W
Thermal Resistance, Junction to Case	R _{eJC}	1.80	°C/W	CW, 85°C, P _{DISS} = 77 W
Case Operating Temperature	Т _с	-40, +140	°C	P _{DISS} = 55 W
Case Operating Temperature	T _c	-40, +85	°C	P _{DISS} = 77 W

Note:

¹ Refer to the Application Note on soldering at <u>www.cree.com/RF/Document-Library</u>

Electrical Characteristics (Frequency = 5.5 GHz to 8.5 GHz unless otherwise stated; T_c = 25°C)

Characteristics	Symbol	Min.	Тур.	Max.	Units	Conditions	
DC Characteristics ¹							
Gate Threshold Voltage	V _{GS(TH)}	-3.8	-3.0	-2.3	V	$V_{_{DS}}$ = 10 V, I $_{_{D}}$ = 13.2 mA	
Gate Quiescent Voltage	V _{GS(Q)}	-	-2.7	-	V _{DC}	V _{DS} = 28 V, I _D = 285 mA	
Saturated Drain Current ²	I _{DS}	10.6	12.8	-	A	$V_{_{ m DS}}$ = 6.0 V, $V_{_{ m GS}}$ = 2.0 V	
Drain-Source Breakdown Voltage	V _{BD}	84	100	-	V	$V_{_{\rm GS}}$ = -8 V, I $_{_{\rm D}}$ = 13.2 mA	
RF Characteristics ³							
Small Signal Gain	S21	18.25	24	-	dB	$V_{_{ m DD}}$ = 28 V, I $_{_{ m DQ}}$ = 285 mA, P $_{_{ m IN}}$ = -20 dBm	
Input Return Loss	S11	-	10	-	dB	$V_{_{DD}}$ = 28 V, I_{_{DQ}} = 285 mA	
Output Return Loss	S22	-	6	-	dB	$V_{_{DD}}$ = 28 V, I $_{_{DQ}}$ = 285 mA	
Output Mismatch Stress	VSWR	-	-	5:1	Ψ	No damage at all phase angles, V_{DD} = 28 V, I_{DQ} = 285 mA, P _{OUT} = 25W OQPSK	

Notes:

¹ Measured on-wafer prior to packaging.

² Scaled from PCM data.

³ Measured in the CMPA5585025F-AMP

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Electrical Characteristics Continued... $(T_c = 25^{\circ}C)$

Characteristics	Symbol	Min.	Тур.	Max.	Units	Conditions
RF Characteristics ^{1,2,3,4}						
Power Added Efficiency	PAE1	24.5	30.0	-	%	$V_{_{DD}}$ = 28 V, $I_{_{DQ}}$ = 285 mA, Frequency = 5.8 GHz
Power Added Efficiency	PAE2	16.5	20.5	-	%	$V_{_{DD}}$ = 28 V, I $_{_{DQ}}$ = 285 mA, Frequency = 7.2 GHz
Power Added Efficiency	PAE3	15.5	19.0	-	%	$V_{_{DD}}$ = 28 V, I $_{_{DQ}}$ = 285 mA, Frequency = 7.9 GHz
Power Added Efficiency	PAE4	15.0	19.5	-	%	$V_{_{DD}}$ = 28 V, I $_{_{DQ}}$ = 285 mA, Frequency = 8.4 GHz
Power Gain	G _{P1}	19.5	21.7	-	dB	$V_{_{DD}}$ = 28 V, I $_{_{DQ}}$ = 285 mA, Frequency = 5.8 GHz
Power Gain	G _{P2}	16.25	17.2	-	dB	$V_{_{DD}}$ = 28 V, I $_{_{DQ}}$ = 285 mA, Frequency = 7.2 GHz
Power Gain	G _{P3}	16.55	18.5	-	dB	$V_{_{DD}}$ = 28 V, I $_{_{DQ}}$ = 285 mA, Frequency = 7.9 GHz
Power Gain	G _{P4}	16.75	18.6	-	dB	$V_{_{DD}}$ = 28 V, I $_{_{DQ}}$ = 285 mA, Frequency = 8.4 GHz
OQPSK Linearity	ACLR1	-	-36	-27.0	dB	$V_{_{DD}}$ = 28 V, I $_{_{DQ}}$ = 285 mA, Frequency = 5.8 GHz
OQPSK Linearity	ACLR2	-	-36	-28.5	dB	$V_{_{DD}}$ = 28 V, I $_{_{DQ}}$ = 285 mA, Frequency = 7.2 GHz
OQPSK Linearity	ACLR3	-	-36	-26.0	dB	$V_{_{DD}}$ = 28 V, I $_{_{DQ}}$ = 285 mA, Frequency = 7.9 GHz
OQPSK Linearity	ACLR4	-	-42	-32.5	dB	$V_{_{DD}}$ = 28 V, I $_{_{DQ}}$ = 285 mA, Frequency = 8.4 GHz

Notes:

¹ Measured in the CMPA5585025F-AMP

² Under OQPSK modulated signal, 1.6 Msps, PN23, Alpha Filter = 0.2.

³ Measured at P_{AVE} = 40 dBm. ⁴ Fixture loss de-embedded.

Electrostatic Discharge (ESD) Classifications

Parameter	Symbol	Class	Test Methodology
Human Body Model	HBM	1A (> 250 V)	JEDEC JESD22 A114-D
Charge Device Model	CDM	II (200 < 500 V)	JEDEC JESD22 C101-C

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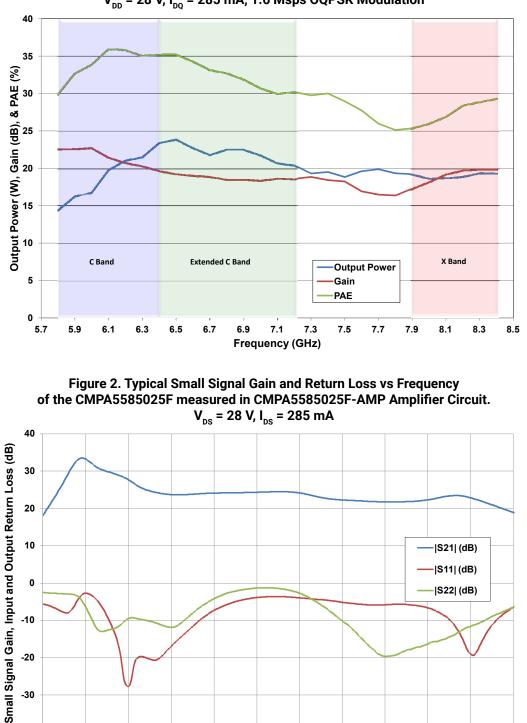


Figure 1. CMPA5585025F Linear Output Power, Gain and PAE at -30 dBc, 1.6 MHz from carrier V_{DD} = 28 V, I_{DO} = 285 mA, 1.6 Msps OQPSK Modulation

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6.5

7.0

7.5

8.0

Frequency (GHz)

8.5

9.0

9.5

10.0

10.5

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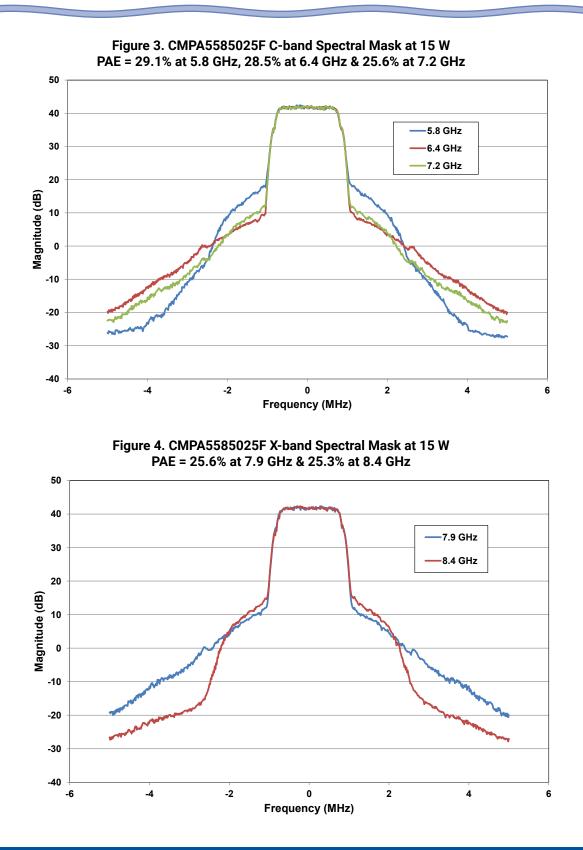
-30

-40 5.0

5.5

6.0





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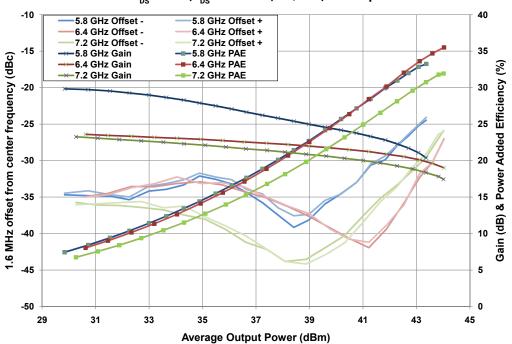
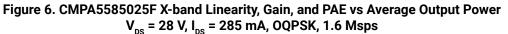
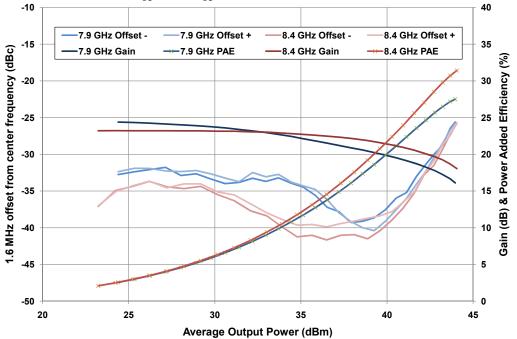


Figure 5. CMPA5585025F C-band Linearity, Gain, and PAE vs Average Output Power $V_{_{DS}}$ = 28 V, $I_{_{DS}}$ = 285 mA, OQPSK, 1.6 Msps





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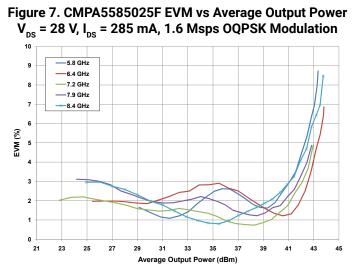
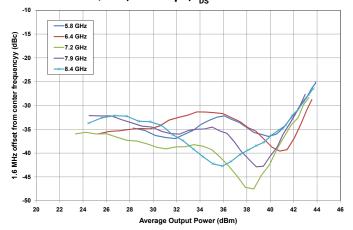
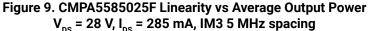
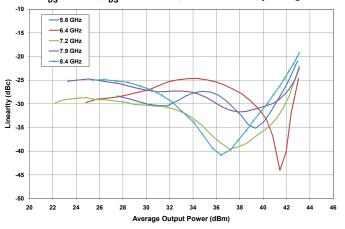


Figure 8. CMPA5585025F - Linearity vs Average Output Power OQPSK, 1.6 Msps, I_{DS} = 285 mA







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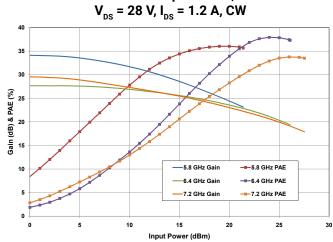
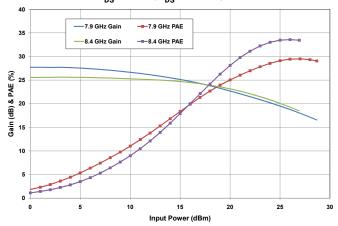
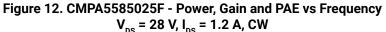
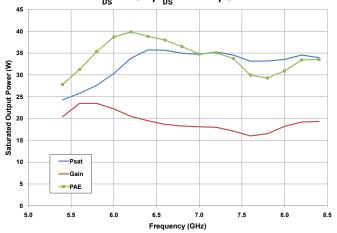


Figure 10. CMPA5585025F - C-band Output Power, Gain and PAE vs Input Power $V_{ra} = 28 V$, $I_{ra} = 1.2 A$, CW

Figure 11. CMPA5585025F - X-band Output Power, Gain and PAE vs Input Power V $_{_{\rm DS}}$ = 28 V, I $_{_{\rm DS}}$ = 1.2 A, CW



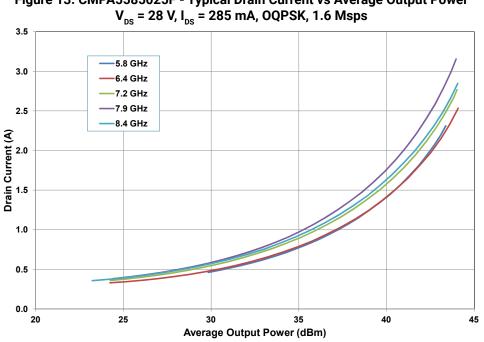




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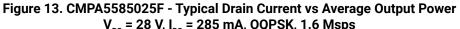
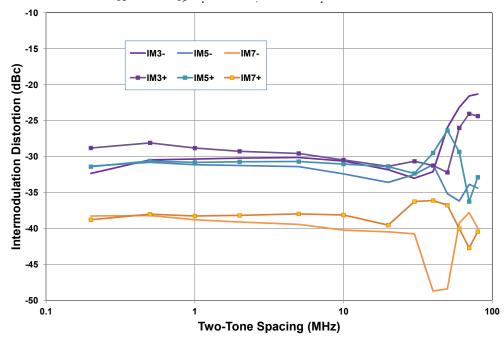


Figure 14. CMPA5585025F - Intermodulation Distortion Products vs Tone Spacing $V_{_{DS}}$ = 28 V, $I_{_{DS}}$ = 285 mA, Center Freq = 7.9 GHz



Note: Divergence in IM5 and IM7 at tone spacings greater than 20 MHz is due to the bias components on the test fixture.

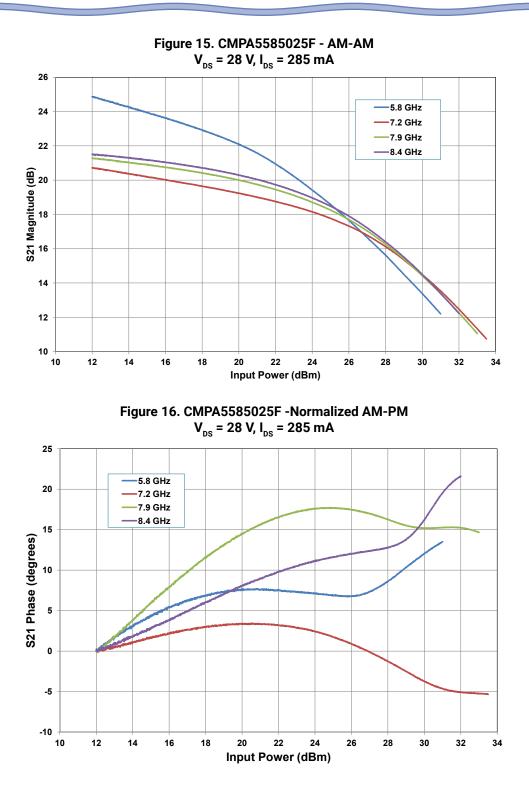
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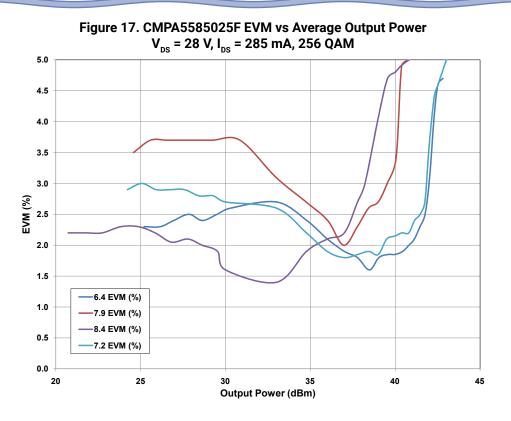


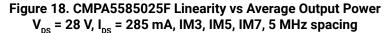


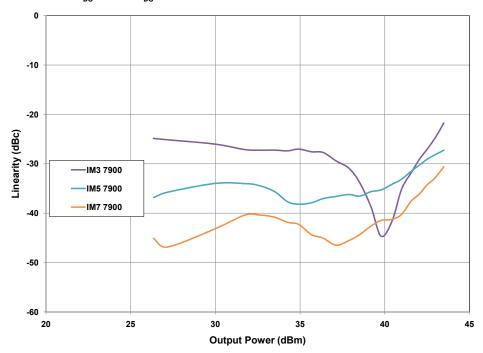
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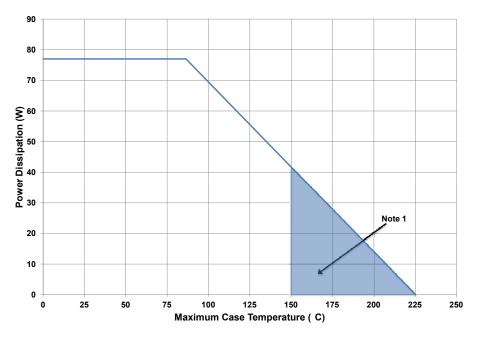


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CMPA5585025F Power Dissipation De-rating Curve



Note 1. Area exceeds Maximum Case Operating Temperature (See Page 2).

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CMPA5585025F-AMP Demonstration Amplifier Circuit Bill of Materials

Designator	Description	Qty
C1, C3, C7, C8, C10, C13	CAP, 1.0 uF, +/-10%, 1210, 100V, X7R	6
C2, C4, C5, C6, C9, C12	CAP, 33000 pF, 0805, 100V, X7R	6
C11, C14	CAP ELECT 3.3UF 80V FK SMD	2
R1, R2	RES 0.0 OHM 1/16W 0402 SMD	2
J1,J2	CONN, SMA, PANEL MOUNT JACK, FLANGE, 4-HOLE, BLUNT POST, 20MIL	2
J3	CONNECTOR, HEADER, RT>PLZ .1CEN LK 9POS	1
-	PCB, TACONIC, RF-35P-0200-CL1/CL1	1
Q1	CMPA5585025F	1

CMPA5585025F-AMP Demonstration Amplifier Circuit

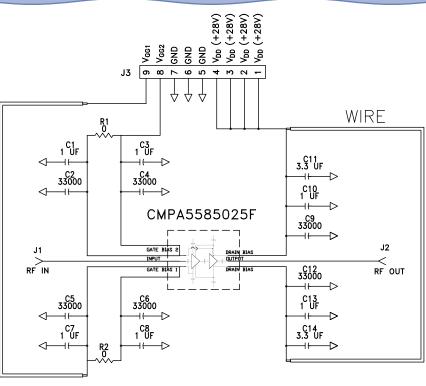


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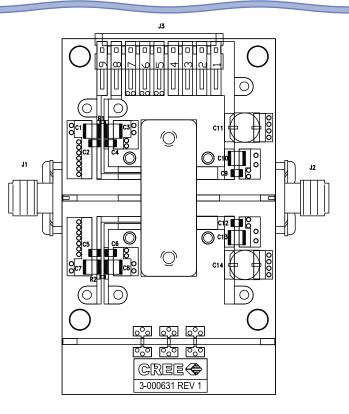
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CMPA5585025F-AMP Demonstration Amplifier Circuit



CMPA5585025F-AMP Demonstration Amplifier Circuit Outline



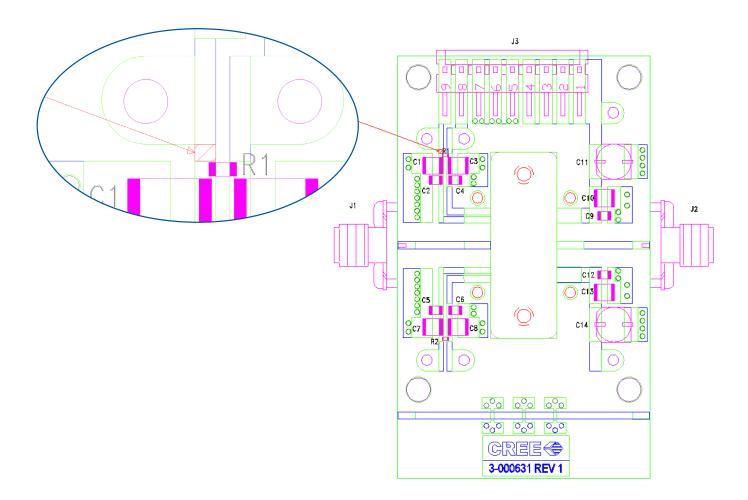
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CMPA5585025F-AMP Demonstration Amplifier Circuit

To configure the CMPA5585025F test fixture to enable independent V_{g1} / V_{g2} control of the device, a cut must be made to the microstrip line just above the R1 resistor as shown. Pin 9 will then supply V_{g1} and Pin 8 will supply V_{g2} .

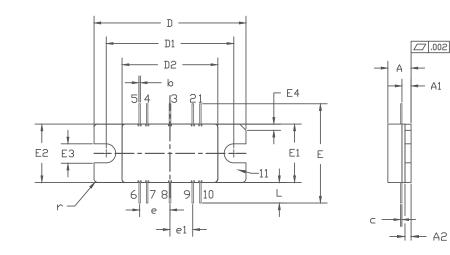


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Product Dimensions CMPA5585025F (Package Type – 440213)



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M - 1994.

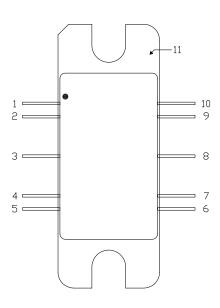
2. CONTROLLING DIMENSION: INCH.

3. ADHESIVE FROM LID MAY EXTEND A MAXIMUM OF 0.020" BEYOND EDGE OF LID.

4. LID MAY BE MISALIGNED TO THE BODY OF PACKAGE BY A MAXIMUM OF 0.008" IN ANY DIRECTION.

	INCHES		MILLIMETERS		N	OTES
DIM	MIN	MAX	MIN	MAX		
Α	0.148	0.168	3.76	4.27		
A1	0.055	0.065	1.40	1.65		
A2	0.035	0.045	0.89	1.14		
b	0.01	TYP	0.254	TYP		10x
с	0.007	0.009	0.18	0.23		
D	0.995	1.005	25.27	25.53		
D1	0.835	0.845	21.21	21.46		
D2	0.623	0.637	15.82	16.18		
Е	0.653	TYP	16.59	TYP		
E1	0.380	0.390	9.65	9.91		
E2	0.380	0.390	9.65	9.91		
E3	0.120	0.130	3.05	3.30		
E4	0.035	0.045	0.89	1.14	45 °	CHAMFER
e	0.20) TYP	5.08 TYP			4x
e1	0.15) TYP	3.81	TYP		4x
L	0.115	0.155	2.92	3.94		10x
r	0.02	5 TYP	.635	TYP		Зx

Pin Number	Qty
1	Gate Bias for Stage 2
2	Gate Bias for Stage 2
3	RF In
4	Gate Bias for Stage 1
5	Gate Bias for Stage 1
6	Drain Bias
7	Drain Bias
8	RF Out
9	Drain Bias
10	Drain Bias
11	Source

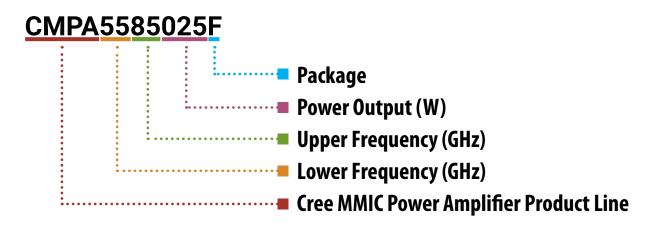


PIN 1: GATE BIAS 6: DRAIN BIAS 2: GATE BIAS 7: DRAIN BIAS 3: RF IN 8: RF DUT 4: GATE BIAS 9: DRAIN BIAS 5: GATE BIAS 10: DRAIN BIAS 11: SDURCE

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Part Number System



Parameter	Value	Units
Lower Frequency	5.5	GHz
Upper Frequency ¹	8.5	GHz
Power Output	25	W
Package	Flange	-



Note¹: Alpha characters used in frequency code indicate a value greater than 9.9 GHz. See Table 2 for value.

Character Code	Code Value
А	0
В	1
С	2
D	3
E	4
F	5
G	6
н	7
J	8
К	9
Examples:	1A = 10.0 GHz 2H = 27.0 GHz

Table 2.

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Product Ordering Information

Order Number	Description	Unit of Measure	Image
CMPA5585025F	GaN MMIC	Each	CREE Contraction of the contract
CMPA5585025F-TB	Test board without GaN MMIC	Each	
CMPA5585025F-AMP	Test board with GaN MMIC installed	Each	

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