

Low noise dual operational amplifier

Datasheet -production data

Features

■ Low voltage noise: 4.5 nV/√Hz

High gain bandwidth product: 15 MHz

■ High slew rate: 7 V/µs■ Low distortion: 0.002%

■ Large output voltage swing: +14.3 V / -14.6 V

■ Low input offset voltage

■ Excellent frequency stability

■ ESD protection 2 kV

■ Macromodel included in this specification

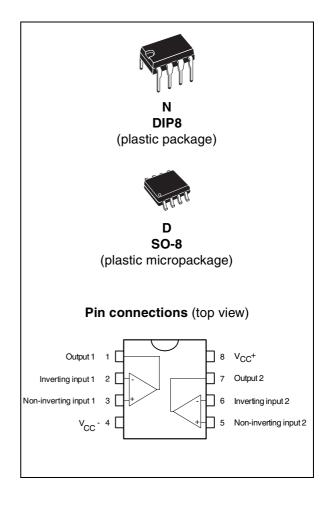
Description

The MC33078 device is a monolithic dual operational amplifier particularly well suited for audio applications.

It offers low voltage noise (4.5 nV/ $\sqrt{\text{Hz}}$) and high frequency performance (15 MHz gain bandwidth product, 7 V/ μ s slew rate).

In addition, the MC33078 device has a very low distortion (0.002%) and excellent phase/gain margins.

The output stage allows a large output voltage swing and symmetrical source and sink currents.



1 Absolute maximum ratings and operating conditions

Table 1. Absolute maximum ratings (AMR)

Symbol	Parameter	Value	Unit
V _{CC}	Supply voltage	±18 or +36	V
V _{id}	Differential input voltage ⁽¹⁾	±30	V
V _i	Input voltage ⁽¹⁾	±15	V
	Output short-circuit duration	Infinite	S
T _j	Junction temperature	+150	°C
T _{stg}	Storage temperature	-65 to +150	°C
R _{thja}	Thermal resistance junction-to-ambient ⁽²⁾ , ⁽³⁾ SO-8 DIP8	125 85	°C/W
R _{thjc}	Thermal resistance junction-to-case ⁽²⁾ , ⁽³⁾ SO-8 DIP8	40 41	°C/W
	HBM: human body model ⁽⁴⁾	2	kV
ESD	MM: machine model ⁽⁵⁾	200	V
	CDM: charged device model ⁽⁶⁾	1.5	kV

- 1. Either or both input voltages must not exceed the magnitude of V_{CC}^+ or V_{CC}^- .
- 2. Short-circuits can cause excessive heating and destructive dissipation.
- 3. R_{th} are typical values.
- 4. Human body model: A 100 pF capacitor is charged to the specified voltage, then discharged through a 1.5 kΩ resistor between two pins of the device. This is done for all couples of connected pin combinations while the other pins are floating.
- 5. Machine model: A 200 pF capacitor is charged to the specified voltage, then discharged directly between two pins of the device with no external series resistor (internal resistor < 5 Ω). This is done for all couples of connected pin combinations while the other pins are floating.
- Charged device model: all pins and the package are charged together to the specified voltage and then discharged directly to ground through only one pin. This is done for all pins.

Table 2. Operating conditions

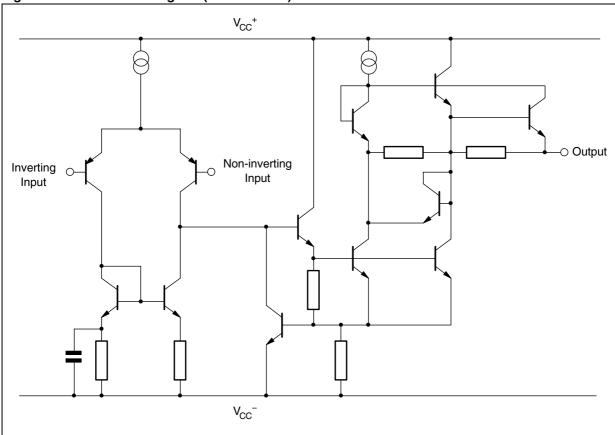
Symbol	Parameter	Value	Unit
V _{CC}	Supply voltage	±2.5 to ±15	V
T _{oper}	Operating free air temperature range	-40 to 125	°C



MC33078 Schematic diagram

2 Schematic diagram

Figure 1. Schematic diagram (1/2 MC33078)



Electrical characteristics MC33078

3 Electrical characteristics

Table 3. $V_{CC}^+ = +15 \text{ V}, V_{CC}^- = -15 \text{ V}, T_{amb} = 25 ^{\circ}\text{C}$ (unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Unit
V _{io}	Input offset voltage ($V_o = 0 \text{ V}, V_{ic} = 0 \text{ V}$) $T_{min} \leq T_{amb} \leq T_{max}$		0.15	2 3	mV
ΔV _{io}	Input offset voltage drift $V_o = 0 \text{ V}, V_{ic} = 0 \text{ V}, T_{min} \le T_{amb} \le T_{max}$		2		μV/°C
I _{io}	Input offset current ($V_o = 0 \text{ V}, V_{ic} = 0 \text{ V}$) $T_{min} \leq T_{amb} \leq T_{max}$		10	150 175	nA
I _{ib}	Input bias current ($V_o = 0 \text{ V}, V_{ic} = 0 \text{ V}$) $T_{min} \leq T_{amb} \leq T_{max}$		250	750 800	nA
V _{icm}	Input common mode voltage range ($\Delta V_{io} = 5 \text{ mV}$, $V_o = 0 \text{ V}$)	±13	±14		V
A _{vd}	Large signal voltage gain (R _L = 2 k Ω V _o = ±10 V) $T_{min} \le T_{amb} \le T_{max}$	90 85	100		dB
±V _{opp}	Output voltage swing (V_{id} = ±1 V) R_L = 600 Ω R_L = 600 Ω R_L = 2.0 k Ω R_L = 2.0 k Ω R_L = 10 k Ω R_L = 10 k Ω	13.2 13.5	12.2 -12.7 14 -14.2 14.3 -14.6	-13.2 -14	V
CMR	Common mode rejection ratio (V _{ic} = ±13 V)	80	100		dB
SVR	Supply voltage rejection ratio $V_{CC}^+ / V_{CC}^- = +15 \text{ V} / -15 \text{ V}$ to +5 V / -5 V	80	105		dB
I _o	Output short-circuit current ($V_{id} = \pm 1$ V, output to ground) Source Sink	15 20	29 37		mA
I _{CC}	Supply current ($V_0 = 0$ V, all amplifiers) $T_{min} \le T_{amb} \le T_{max}$		4	5 5.5	mA
SR	Slew rate V_i = -10 V to +10 V, R_L = 2 k Ω , C_L = 100 pF, A_V = +1	5	7		V/µs
GBP	Gain bandwidth product $R_L = 2 \ k\Omega$, $C_L = 100 \ pF$, $f = 100 \ kHz$	10	15		MHz
В	Unity gain bandwidth (open loop)		9		MHz
A _m	Gain margin (R _L = 2 k Ω), C _L = 0 pF C _L = 100 pF		-11 -6		dB
φm	Phase margin (R _L = 2 k Ω), C _L = 0 pF C _L = 100 pF		55 30		Degrees
e _n	Equivalent input noise voltage $R_S = 100 \ \Omega \ f = 1 \ kHz$		4.5		nV/√Hz
i _n	Equivalent input noise current (f = 1 kHz)		0.5		pA/√Hz

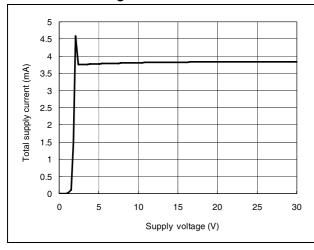
Table 3. $V_{CC}^+ = +15 \text{ V}, V_{CC}^- = -15 \text{ V}, T_{amb} = 25 ^{\circ}\text{C}$ (unless otherwise specified) (continued)

Symbol	Parameter	Min.	Тур.	Max.	Unit
THD	Total harmonic distortion $R_L=2~k\Omega$ f = 20 Hz to 20 kHz, $V_o=3~V_{rms},~A_V=+1$		0.002		%
V _{O1} /V _{O2}	Channel separation f = 20 Hz to 20 kHz		120		dB
FPB	Full power bandwidth $V_{o} = 27 \ V_{pp}, \ R_{L} = 2 \ k\Omega, \ THD \leq 1\%$		120		kHz
Z _o	Output impedance $V_0 = 0 \text{ V}, f = 9 \text{ MHz}$		37		Ω
R _i	Input resistance $V_{ic} = 0 V$		175		kΩ
C _i	Input capacitance $V_{ic} = 0 V$		12		pF

Electrical characteristics MC33078

Figure 2. Total supply current vs. supply voltage

Figure 3. Output voltage vs. supply voltage



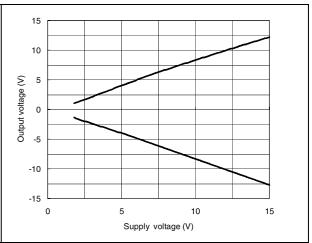
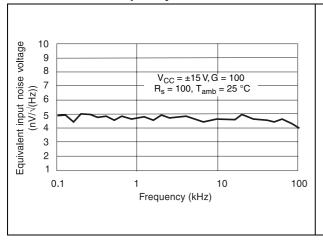


Figure 4. Equivalent input noise voltage vs. frequency

Figure 5. Output short-circuit current vs. output voltage



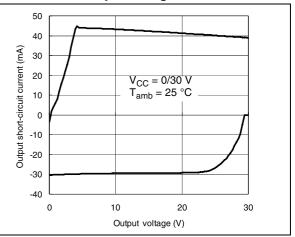
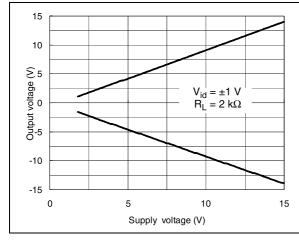
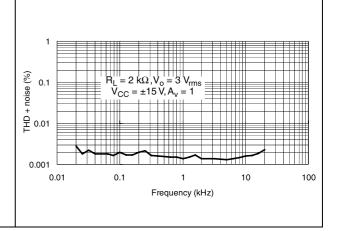


Figure 6. Output voltage vs. supply voltage ($V_{id} = \pm 1$ V, $R_L = 2$ k Ω)

Figure 7. THD + noise vs. frequency

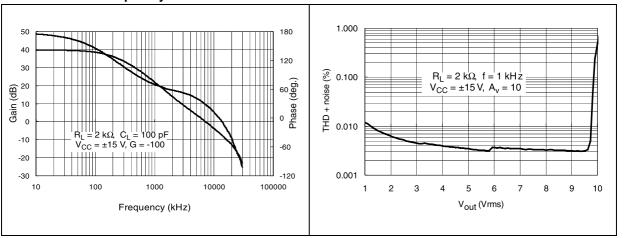




577

Figure 8. Voltage gain and phase vs. frequency

Figure 9. THD noise vs. V_{out}



Macromodel MC33078

4 Macromodel

4.1 Important note concerning this macromodel

Please consider the following remarks before using this macromodel.

- All models are a trade-off between accuracy and complexity (i.e. simulation time).
- Macromodels are not a substitute to breadboarding; rather, they confirm the validity of a design approach and help to select surrounding component values.
- A macromodel emulates the nominal performance of a typical device within specified operating conditions (temperature, supply voltage, for example). Thus the macromodel is often not as exhaustive as the datasheet, its purpose is to illustrate the main parameters of the product.

Data derived from macromodels used outside of the specified conditions (V_{CC} , temperature, for example) or even worse, outside of the device operating conditions (V_{CC} , V_{icm} , for example), is not reliable in any way.

Section 4.2 provides the electrical characteristics resulting from the use of this macromodel.

4.2 Electrical characteristics from macromodelization

Table 4. Electrical characteristics resulting from macromodel simulation at V_{CC}^+ = +15 V, V_{CC}^- = -15 V, T_{amb} = 25 °C (unless otherwise specified)

Symbol	Conditions	Value	Unit
V _{io}		0	mV
A _{VD}	$R_L = 2 k\Omega$, $V_O = \pm 10 V$	100	dB
I _{CC}	No load, per operator	2	mA
V _{icm}	$\Delta V_{io} = 5 \text{ mV}, V_O = 0 \text{ V}$	28	V
V _{opp}	$R_L = 2 k\Omega$	28.2	V
I _{sink}	V _O = 0 V	37	mA
I _{source}	V _O = 0 V	29	mA
GBP	GBP $R_L = 2 \text{ k}\Omega$, $C_L = 100 \text{ pF}$		MHz
SR	$R_L = 10 \text{ k}\Omega, C_L = 100 \text{ pF}, A_V = +1$	7	V/µs
φm	$R_L = 2 k\Omega$, $C_L = 0 pF$	55	Degrees

MC33078 Macromodel

4.3 Macromodel code

```
** Standard Linear Ics Macromodels, 1993.
** CONNECTIONS :
* 1 INVERTING INPUT
* 2 NON-INVERTING INPUT
* 3 OUTPUT
* 4 POSITIVE POWER SUPPLY
* 5 NEGATIVE POWER SUPPLY
.SUBCKT MC33078 1 3 2 4 5
************
.MODEL MDTH D IS=1E-8 KF=2.286238E-16 CJO=10F
* INPUT STAGE
CIP 2 5 1.200000E-11
CIN 1 5 1.200000E-11
EIP 10 5 2 5 1
EIN 16 5 1 5 1
RIP 10 11 2.363636E+00
RIN 15 16 2.363636E+00
RIS 11 15 1.224040E+01
DIP 11 12 MDTH 400E-12
DIN 15 14 MDTH 400E-12
VOFP 12 13 DC 0
VOFN 13 14 DC 0
IPOL 13 5 1.100000E-04
CPS 11 15 2.35E-09
DINN 17 13 MDTH 400E-12
VIN 17 5 1.000000e+00
DINR 15 18 MDTH 400E-12
VIP 4 18 1.000000E+00
FCP 4 5 VOFP 1.718182E+01
FCN 5 4 VOFN 1.718182E+01
FIBP 2 5 VOFN 4.545455E-03
FIBN 5 1 VOFP 4.545455E-03
* AMPLIFYING STAGE
FIP 5 19 VOFP 9.545455E+02
FIN 5 19 VOFN 9.545455E+02
CC 19 29 1.500000E-08
HZTP 30 29 VOFP 1.523529E+02
HZTN 5 30 VOFN 1.523529E+02
DOPM 51 22 MDTH 400E-12
DONM 21 52 MDTH 400E-12
HOPM 22 28 VOUT 5.172414E+03
VIPM 28 4 1.500000E+02
HONM 21 27 VOUT 4.054054E+03
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Macromodel MC33078

```
VINM 5 27 1.500000E+02
DBIDON1 19 53 MDTH 400E-12
V1 51 53 0.68
DBIDON2 54 19 MDTH 400E-12
V2 54 52 0.68
RG11 51 5 3.04E+05
RG12 51 4 3.04E+05
RG21 52 5 0.6072E+05
RG22 52 4 0.6072E+05
E1 50 40 51 0 1 E2 40 39 52 0 1
EDEC1 38 39 4 0 0.5
EDEC2 0 38 5 0 0.5
DOP 51 25 MDTH 400E-12
VOP 4 25 1.474575E+00
DON 24 52 MDTH 400E-12
VON 24 5 1.474575E+00
RAJUS 50 5 1E12
GCOMP 5 4 4 5 8.1566068E-04
RPM1 5 80 1E+06
RPM2 4 80 1E+06
GAVPH 5 82 50 80 3.26E-03
RAVPHGH 82 4 613
RAVPHGB 82 5 613
RAVPHDH 82 83 1000
RAVPHDB 82 84 1000
CAVPHH 4 83 0.159E-09
CAVPHB 5 84 0.159E-09
EOUT 26 23 82 5 1
VOUT 23 5 0
ROUT 26 3 4.780354E+01
COUT 3 5 1.000000E-12
```

.ENDS

10/15

MC33078 Package information

5 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

Package information MC33078

5.1 DIP8 package information

Figure 10. DIP8 package outline

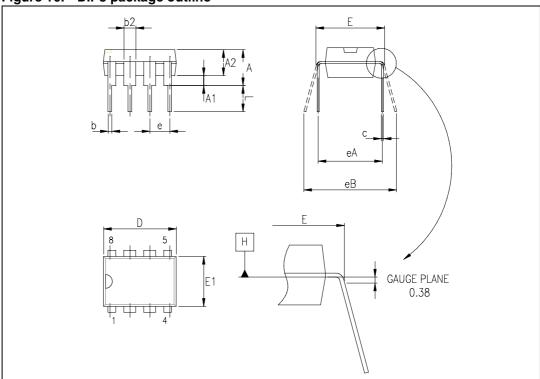


Table 5. DIP8 package mechanical data

			Dimer	nsions			
Symbol		Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.	
Α			5.33			0.210	
A1	0.38			0.015			
A2	2.92	3.30	4.95	0.115	0.130	0.195	
b	0.36	0.46	0.56	0.014	0.018	0.022	
b2	1.14	1.52	1.78	0.045	0.060	0.070	
С	0.20	0.25	0.36	0.008	0.010	0.014	
D	9.02	9.27	10.16	0.355	0.365	0.400	
E	7.62	7.87	8.26	0.300	0.310	0.325	
E1	6.10	6.35	7.11	0.240	0.250	0.280	
е		2.54			0.100		
eA		7.62			0.300		
eB			10.92			0.430	
L	2.92	3.30	3.81	0.115	0.130	0.150	

5.2 SO-8 package information

Figure 11. SO-8 package outline

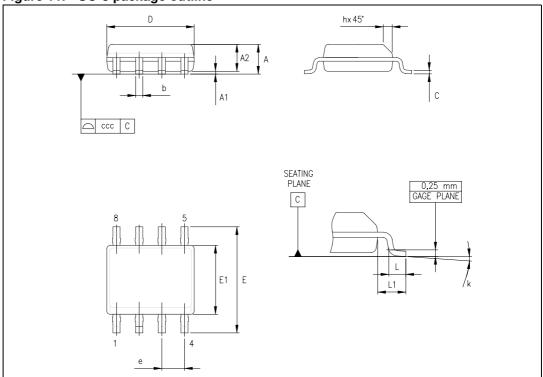


Table 6. SO-8 package mechanical data

			Dimer	nsions		
Symbol	Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α			1.75			0.069
A1	0.10		0.25	0.004		0.010
A2	1.25			0.049		
b	0.28	0.40	0.48	0.011	0.016	0.019
С	0.17		0.23	0.007		0.010
D	4.80	4.90	5.00	0.189	0.193	0.197
Е	5.80	6.00	6.20	0.228	0.236	0.244
E1	3.80	3.90	4.00	0.150	0.154	0.157
е		1.27			0.050	
h	0.25		0.50	0.010		0.020
L	0.40	0.635	1.27	0.016	0.025	0.050
k	1°		8°	1°		8°
ccc			0.10			0.004

Ordering information MC33078

6 Ordering information

Table 7. Order codes

Order code	de Temperature Package Packaging		Marking	
MC33078N		DIP8	Tube	MC33078N
MC33078D MC33078DT	-40, +125 °C	SO-8	Tube or tape and reel	33078
MC33078YDT ⁽¹⁾		SO-8 (automotive grade)	Tube or tape and reel	33078Y

Qualified and characterized according to AEC Q100 and Q003 or equivalent, advanced screening according to AEC Q001 and Q 002 or equivalent.

7 Revision history

Table 8. Document revision history

Date	Revision	Changes
10-Nov-2001	1	Initial release.
10-Jun-2005	2	PPAP references inserted in the datasheet, see order codes table.
30-Sep- 2005	3	The following changes were made in this revision: - Order codes table updated with complete list of markings and packages. - Reorganization of <i>Chapter 4.3: Macromodel code on page 9.</i>
16-Feb-2006	4	Corrected error in the first page title.
3-Mar-2008 5		Added R _{th} and ESD values in absolute maximum ratings table. Added footnote for automotive grade order codes in order codes table. Re-formatted document.
13-Nov-2012 6		Added conditions to title of <i>Figure 6</i> . Updated temperature range in <i>Table 2</i> and <i>Table 7</i> . Updated ECOPACK text in <i>Section 5</i> . Updated <i>Table 6</i> (added "b" and "L" typ. dimensions). Qualified status of the MC33078YDT order code, removed the MC33078YD order code from <i>Table 7</i> . Minor corrections throughout document.

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