

High Efficiency Standard Rectifier

800 V V_{RRM}

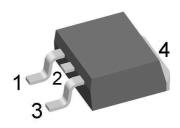
20 A

1.24 V

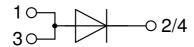
Single Diode

Part number

DLA20IM800PC



Backside: cathode



Features / Advantages:

- Planar passivated chips
- Very low leakage current
- Very low forward voltage drop
- Improved thermal behaviour

Applications:

- Diode for main rectification
- For single and three phase bridge configurations

Package: TO-263 (D2Pak)

- Industry standard outline
- RoHS compliant
- Epoxy meets UL 94V-0

Terms _Conditions of usage:

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact the sales office, which is responsible for you.

Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact the sales office, which is responsible for you.

Should you intend to use the product in aviation, in health or live endangering or life support applications, please notify. For any such application we urgently recommend

to perform joint risk and quality assessments;
the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747 and per semiconductor unless otherwise specified

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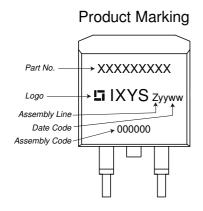


Rectifier				Ratings			
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V _{RSM}	max. non-repetitive reverse bloc	king voltage	$T_{VJ} = 25^{\circ}C$			900	V
V _{RRM}	max. repetitive reverse blocking	voltage	$T_{VJ} = 25^{\circ}C$			800	V
I _R	reverse current	$V_R = 800 \text{ V}$	$T_{VJ} = 25^{\circ}C$			5	μΑ
		$V_R = 800 V$	$T_{VJ} = 150$ °C			0.05	mΑ
V _F	forward voltage drop	I _F = 20 A	$T_{VJ} = 25^{\circ}C$			1.27	V
		$I_F = 40 \text{ A}$				1.49	٧
		I _F = 20 A	T _{VJ} = 150 °C			1.24	V
		$I_F = 40 \text{ A}$				1.59	٧
I FAV	average forward current	T _C = 140°C	$T_{VJ} = 175$ °C			20	Α
		rectangular d = 0.5					i ! !
V _{F0}	threshold voltage		T _{vJ} = 175°C			0.86	٧
r _F	slope resistance } for power	loss calculation only				19	mΩ
R _{thJC}	thermal resistance junction to ca	ase				1	K/W
R _{thCH}	thermal resistance case to heats	sink			0.25		K/W
P _{tot}	total power dissipation		$T_{C} = 25^{\circ}C$			150	W
I _{FSM}	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			200	Α
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			215	Α
		t = 10 ms; (50 Hz), sine	T _{vJ} = 150°C			170	Α
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			185	Α
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			200	A ² s
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			190	A²s
		t = 10 ms; (50 Hz), sine	$T_{VJ} = 150$ °C			145	A ² s
		t = 8.3 ms; (60 Hz), sine	$V_R = 0 V$			140	A²s
CJ	junction capacitance	$V_R = 400 \text{ V}; f = 1 \text{ MHz}$	$T_{VJ} = 25^{\circ}C$		4		pF



Package TO-263 (D2Pak)				Ratings			
Symbol	Definition	Conditions	min.	typ.	max.	Unit	
I _{RMS}	RMS current	per terminal 1)			35	Α	
T _{VJ}	virtual junction temperature		-55		175	°C	
T _{op}	operation temperature		-55		150	°C	
T _{stg}	storage temperature		-55		150	°C	
Weight				2		g	
F _c	mounting force with clip		20		60	N	

¹⁾ l_{nusc} is typically limited by the pin-to-chip resistance (1); or by the current capability of the chip (2). In case of (1) and a product with multiple pins for one chip-potential, the current capability can be increased by connecting the pins as one contact.



Part description

D = Diode

L = High Efficiency Standard Rectifier

A = (up to 1200V)

20 = Current Rating [A]

IM = Single Diode

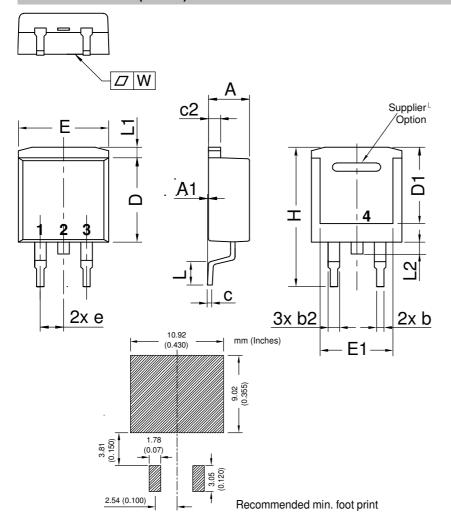
800 = Reverse Voltage [V] PC = TO-263AB (D2Pak) (2)

Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	DLA20IM800PC	DLA20IM800PC	Tape & Reel	800	506475
Alternative	DLA20IM800PC-TUB	DLA20IM800PC	Tube	50	506628

Equiva	alent Circuits for	Simulation	* on die level	$T_{VJ} = 175 ^{\circ}\text{C}$
$I \rightarrow V_0$)—[R ₀]-	Rectifier		
V _{0 max}	threshold voltage	0.86		V
$R_{0 \text{ max}}$	slope resistance *	16		$m\Omega$

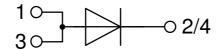


Outlines TO-263 (D2Pak)



	Millimeter		Inches		
Dim.		neter		nes	
	min	max	min	max	
Α	4.06	4.83	0.160	0.190	
A1	typ.	0.10	typ. C	0.004	
A2	2.4	41	0.095		
b	0.51	0.99	0.020	0.039	
b2	1.14	1.40	0.045	0.055	
С	0.40	0.74	0.016	0.029	
c2	1.14	1.40	0.045	0.055	
D	8.38	9.40	0.330	0.370	
D1	8.00	8.89	0.315	0.350	
D2	2.5		0.098		
Е	9.65	10.41	0.380	0.410	
E1	6.22	8.50	0.245	0.335	
е	2,54 BSC		0,100 BSC		
e1	4.28		0.169		
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	1.02	1.68	0.040	0.066	
W	typ. 0.02	0.040	typ. 0.0008	0.002	

All dimensions conform with and/or within JEDEC standard.





Rectifier

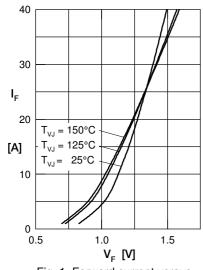


Fig. 1 Forward current versus voltage drop

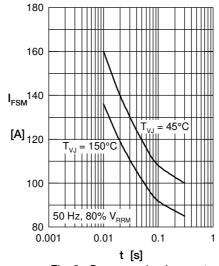


Fig. 2 Surge overload current

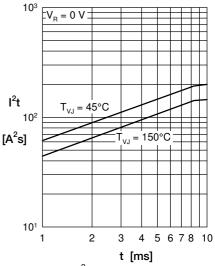


Fig. 3 I2t versus time

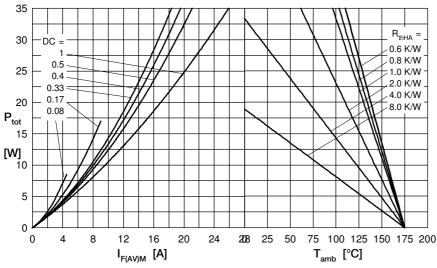


Fig. 4 Power dissipation versus direct output current and ambient temperature

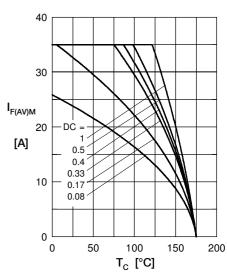


Fig. 5 Max. forward current vs. case temperature

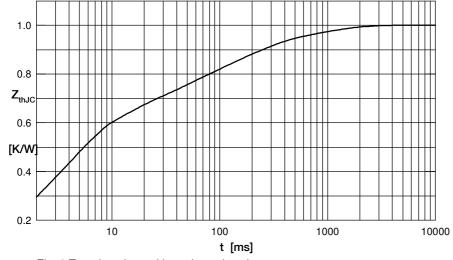


Fig. 6 Transient thermal impedance junction to case

Constants for Z_{thJC} calculation:

i	R_{thi} (K/W)	t _i (s)
1	0.51	0.0035
2	0.06	0.0003
3	0.14	0.025
4	0.09	8.0
5	0.2	0.14

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