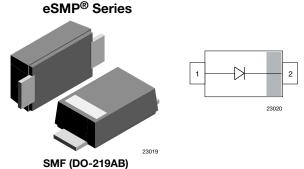


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# Standard Recovery Rectifier, High Voltage Surface Mount



#### **FEATURES**

• For surface mounted applications



· Ideal for automated placement

Glass passivated

 High temperature soldering: 260 °C / 10 s at terminals

ROHS COMPLIANT HALOGEN

FREE

• Wave and reflow solderable

 Compatible to SOD-123W package case outline or SOD-123F and SOD-123FL

 Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>



Case: SMF (DO-219AB)

Polarity: band denotes cathode end

Weight: approx. 15 mg
Packaging codes / options:

18/10K per 13" reel (8 mm tape), MOQ = 50K 08/3K per 7" reel (8 mm tape), MOQ = 30K

Circuit configuration: single

#### **ADDITIONAL RESOURCES**



PARTS TABLE								
PART	ORDERING CODE	MARKING		REMARKS				
S1FLB-M	S1FLB-M-18 or S1FLB-M-08	НВ		Tape and reel				
S1FLD-M	S1FLD-M-18 or S1FLD-M-08	HD		Tape and reel				
S1FLG-M	S1FLG-M-18 or S1FLG-M-08	HG		Tape and reel				
S1FLJ-M	S1FLJ-M-18 or S1FLJ-M-08	HJ		Tape and reel				
S1FLK-M	S1FLK-M-18 or S1FLK-M-08	HK		Tape and reel				
S1FLM-M	S1FLM-M-18 or S1FLM-M-08	HM		Tape and reel				
			•					
ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT		
		S1FLB-M	$V_{RRM}$	100	V		
		S1FLD-M	$V_{RRM}$	200	V		
Maximum rapatitiva paak rayaraa valtaga		S1FLG-M	$V_{RRM}$	400	V		
Maximum repetitive peak reverse voltage		S1FLJ-M	$V_{RRM}$	600	V		
		S1FLK-M	$V_{RRM}$	800	V		
		S1FLM-M	$V_{RRM}$	1000	V		
		S1FLB-M	V <sub>RMS</sub>	70	V		
		S1FLD-M	$V_{RMS}$	140	V		
Maximum RMS voltage		S1FLG-M	$V_{RMS}$	280	V		
Waximum riwo voltage		S1FLJ-M	$V_{RMS}$	420	V		
		S1FLK-M	$V_{RMS}$	560	V		
		S1FLM-M	$V_{RMS}$	700	V		
		S1FLB-M	$V_{DC}$	100	V		
		S1FLD-M	$V_{DC}$	200	V		
Maximum DC blocking voltage		S1FLG-M	$V_{DC}$	400	V		
Maximum DC blocking voltage		S1FLJ-M	$V_{DC}$	600	V		
		S1FLK-M	$V_{DC}$	800	V		
		S1FLM-M	$V_{DC}$	1000	V		
	$T_L = 75  ^{\circ}C^{(1)}$		I <sub>F(AV)</sub>	1.5	Α		
Maximum average forward rectified current	$T_A = 25  ^{\circ}\text{C}^{(1)}$ at $R_{thJA} < 110  \text{K/W}$		I <sub>F(AV)</sub>	1	Α		
	$T_A = 65  ^{\circ}C^{(1)}$		I <sub>F(AV)</sub>	0.7	Α		
Peak forward surge current 8.3 ms half sine-wave $T_L = 25$ °C $I_{FSM}$ 2				22	Α		

#### Note

(1) Averaged over any 20 ms period

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THERMAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
Thermal resistance junction to ambient air (1)		$R_{thJA}$	180	K/W			
Operating junction and storage temperature range		T <sub>j</sub> , T <sub>stg</sub>	-55 to +150	°C			

#### Note

<sup>(1)</sup> Mounted on epoxy substrate with 3 mm x 3 mm Cu pads (≥ 40 µm thick)

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
	1 A <sup>(1)</sup>	S1FLB-M	$V_{F}$			1.1	V
		S1FLD-M	$V_{F}$			1.1	V
Lastanta da cara de la companione		S1FLG-M	$V_{F}$			1.1	V
Instantaneous forward voltage		S1FLJ-M	$V_{F}$			1.1	V
		S1FLK-M	$V_{F}$			1.1	V
		S1FLM-M	$V_{F}$			1.1	V
	T <sub>A</sub> = 25 °C	S1FLB-M	I <sub>R</sub>			10	μA
		S1FLD-M	I <sub>R</sub>			10	μA
		S1FLG-M	I <sub>R</sub>			10	μΑ
		S1FLJ-M	I <sub>R</sub>			10	μA
		S1FLK-M	I <sub>R</sub>			10	μA
Maximum DC reverse current at rated		S1FLM-M	I <sub>R</sub>			10	μA
DC blocking voltage	T <sub>A</sub> = 125 °C	S1FLB-M	I <sub>R</sub>			50	μA
		S1FLD-M	I <sub>R</sub>			50	μΑ
		S1FLG-M	I <sub>R</sub>			50	μA
		S1FLJ-M	I <sub>R</sub>			50	μΑ
		S1FLK-M	I <sub>R</sub>			50	μA
		S1FLM-M	I <sub>R</sub>			50	μA
	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, I <sub>rr</sub> = 0.25 A	S1FLB-M	t <sub>rr</sub>			1800	ns
		S1FLD-M	t <sub>rr</sub>			1800	ns
D		S1FLG-M	t <sub>rr</sub>			1800	ns
Reverse recovery time		S1FLJ-M	t <sub>rr</sub>			1800	ns
		S1FLK-M	t <sub>rr</sub>			1800	ns
		S1FLM-M	t <sub>rr</sub>			1800	ns
	4 V, 1 MHz	S1FLB-M	Cj		4		pF
		S1FLD-M	Cj		4		pF
Tomical agrapheses		S1FLG-M	Cj		4		pF
Typical capacitance		S1FLJ-M	Cj		4		pF
		S1FLK-M	Cj		4		pF
		S1FLM-M	Cj		4		pF

#### Note

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

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### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

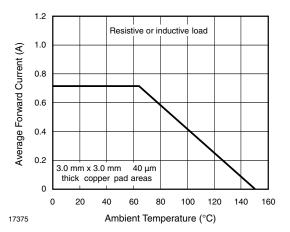


Fig. 1 - Forward Current Derating Curve

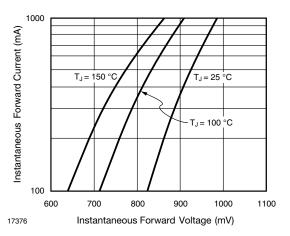


Fig. 2 - Typical Instantaneous Forward Characteristics

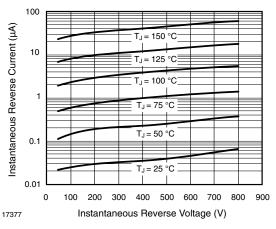


Fig. 3 - Typical Instantaneous Reverse Characteristics

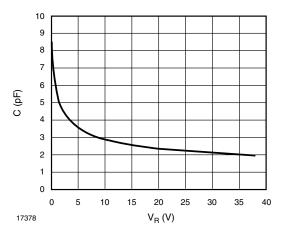
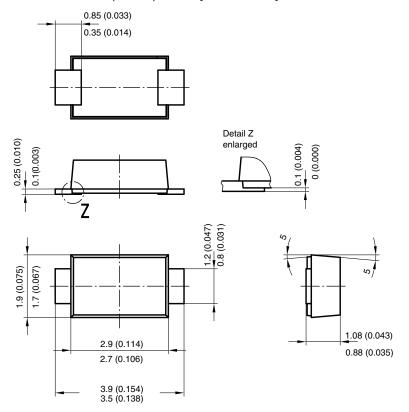


Fig. 4 - Capacitance vs. Reverse Voltage

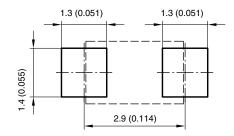
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### PACKAGE DIMENSIONS in millimeters (inches): SMF (DO-219AB)

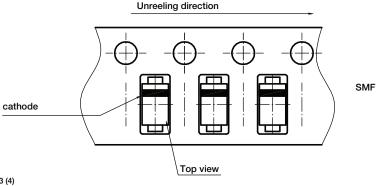


#### Foot print recommendation:



Created - Date: 15. February 2005 Rev. 3 - Date: 13. March 2007 Document no.: S8-V-3915.01-001 (4) 17247

### **ORIENTATION IN CARRIER TAPE - SMF**



Document no.: S8-V-3717.02-003 (4) Created - Date: 09. Feb. 2010

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Rev.1.4, 27-Apr-2020 4 Document Number: 82453 For technical questions, contact: DiodesAmericas@vishay.com



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