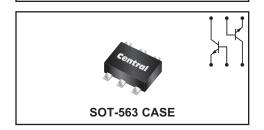


SURFACE MOUNT SILICON DUAL, HIGH VOLTAGE NPN TRANSISTOR



Central Semiconductor Corp.

www.centralsemi.com

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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMLT5551 consists of two individual, isolated NPN silicon transistors manufactured by the epitaxial planar process and epoxy molded in an SOT-563 suface mount package. This device has been designed for high voltage amplifier applications.

MARKING CODE: 5C5

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Collector-Base Voltage	V _{CBO}	180	V
Collector-Emitter Voltage	VCEO	160	V
Emitter-Base Voltage	V _{EBO}	6.0	V
Continuous Collector Current	IC	600	mA
Power Dissipation	PD	350	mW
Operating and Storage Junction Temperature	TJ, T _{stg}	-65 to +150	°C
Thermal Resistance	Θ_{JA}	357	°C/W

ELECTRICAL CHARACTERISTICS PER TRANSISTOR: (T_A=25°C unless otherwise noted) SYMBOL TEST CONDITIONS MIN MAX

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
ICBO	V _{CB} =120V		50	nA
ICBO	V _{CB} =120V, T _A =100°C		50	μA
BVCBO	Ι _C =100μΑ	180		V
BVCEO	I _C =1.0mA	160		V
BVEBO	I _E =10μA	6.0		V
V _{CE(SAT)}	I _C =10mA, I _B =1.0mA		0.15	V
V _{CE(SAT)}	I _C =50mA, I _B =5.0mA		0.20	V
V _{BE(SAT)}	I _C =10mA, I _B =1.0mA		1.00	V
V _{BE(SAT)}	I _C =50mA, I _B =5.0mA		1.00	V
hFE	V _{CE} =5.0V, I _C =1.0mA	80		
h _{FE}	V _{CE} =5.0V, I _C =10mA	80	250	
hFE	V _{CE} =5.0V, I _C =50mA	30		
fT	V_{CE} =10V, I _C =10mA, f=100MHz	100	300	MHz
Cob	V _{CB} =10V, I _E =0, f=1.0MHz		6.0	pF
h _{fe}	V_{CE} =10V, I _C =1.0mA, f=1.0kHz	50	200	
NF	V_{CE} =5.0V, I _C =200µA, R _S =10Ω,			
	f=10Hz to 15.7kHz		8.0	dB

R3 (29-June 2015)

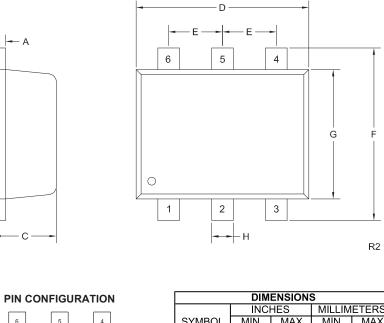


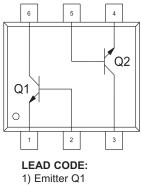


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SOT-563 CASE - MECHANICAL OUTLINE





DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
A	0.0027	0.007	0.07	0.18			
B	0.008		0.20				
C	0.017	0.024	0.45	0.60			
D	0.059	0.067	1.50	1.70			
E	0.020		0.50				
F	0.059	0.067	1.50	1.70			
G	0.043	0.051	1.10	1.30			
Н	0.006	0.012	0.15	0.30			
SOT-563 (REV: R2)							

1) Emitter Q1
2) Base Q1
3) Collector Q2
4) Emitter Q2
5) Base Q2

6) Collector Q1

MARKING CODE: 5C5

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OUTSTANDING SUPPORT AND SUPERIOR SERVICES

PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- · Environmental regulation compliance
- Customer specific screening
- · Up-screening capabilities

· Custom product packing

Custom bar coding for shipments

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

REQUESTING PRODUCT PLATING

- If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when 1. ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix " PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

Corporate Headquarters & Customer Support Team

Central Semiconductor Corp. 145 Adams Avenue Hauppauge, NY 11788 USA Main Tel: (631) 435-1110 Main Fax: (631) 435-1824 Support Team Fax: (631) 435-3388 www.centralsemi.com

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